

Fig. 1

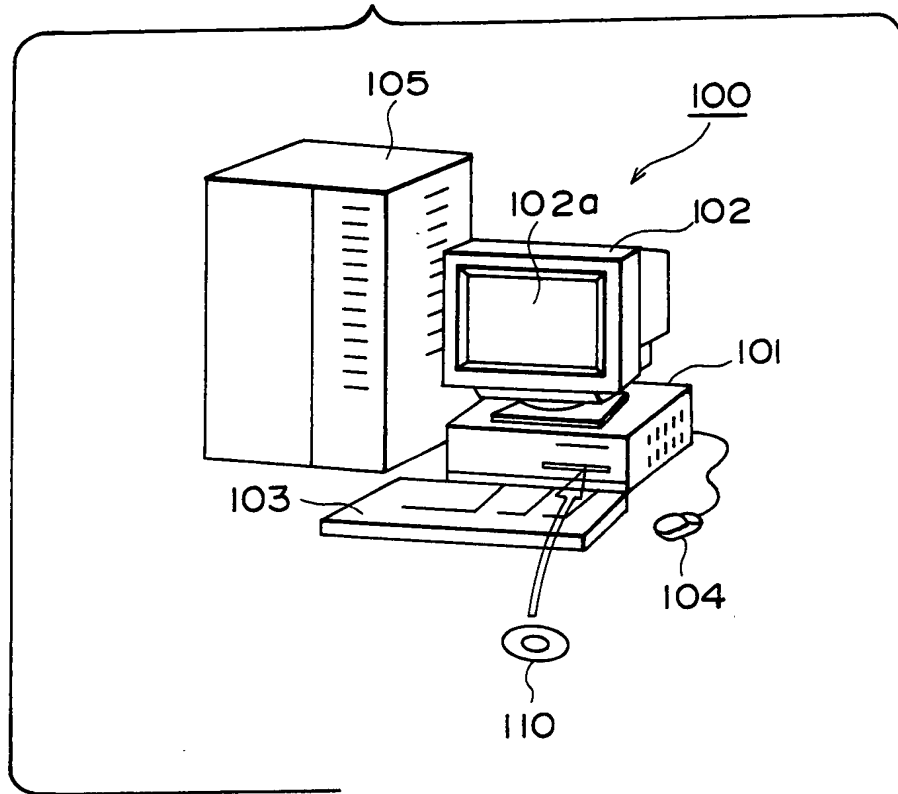
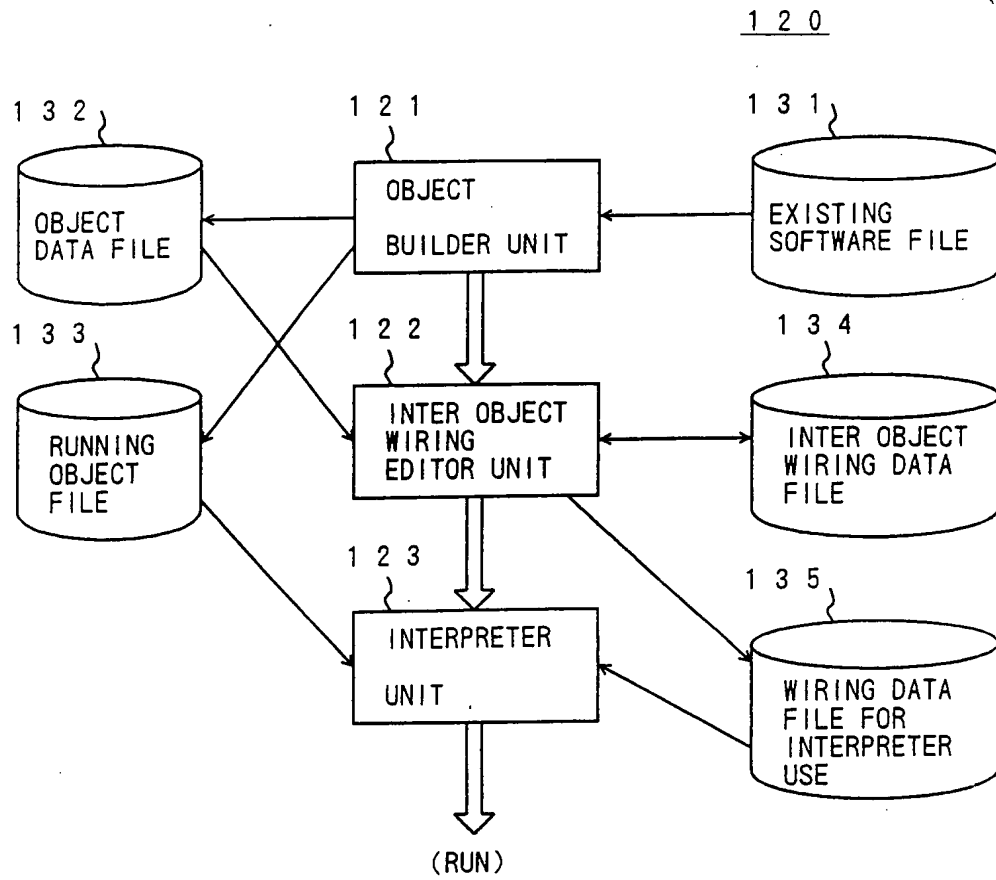


Fig. 2



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Fig. 3

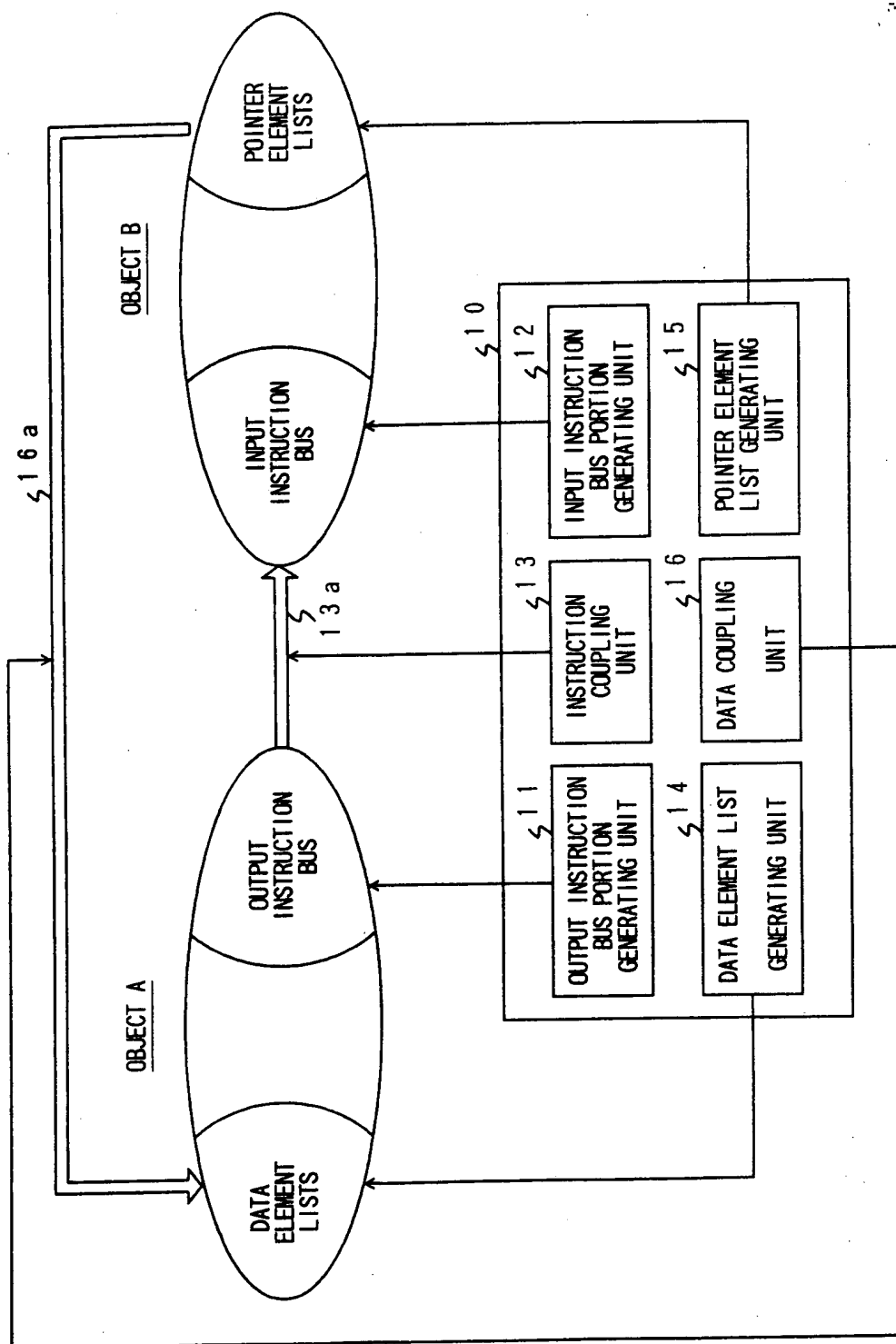


Fig. 4

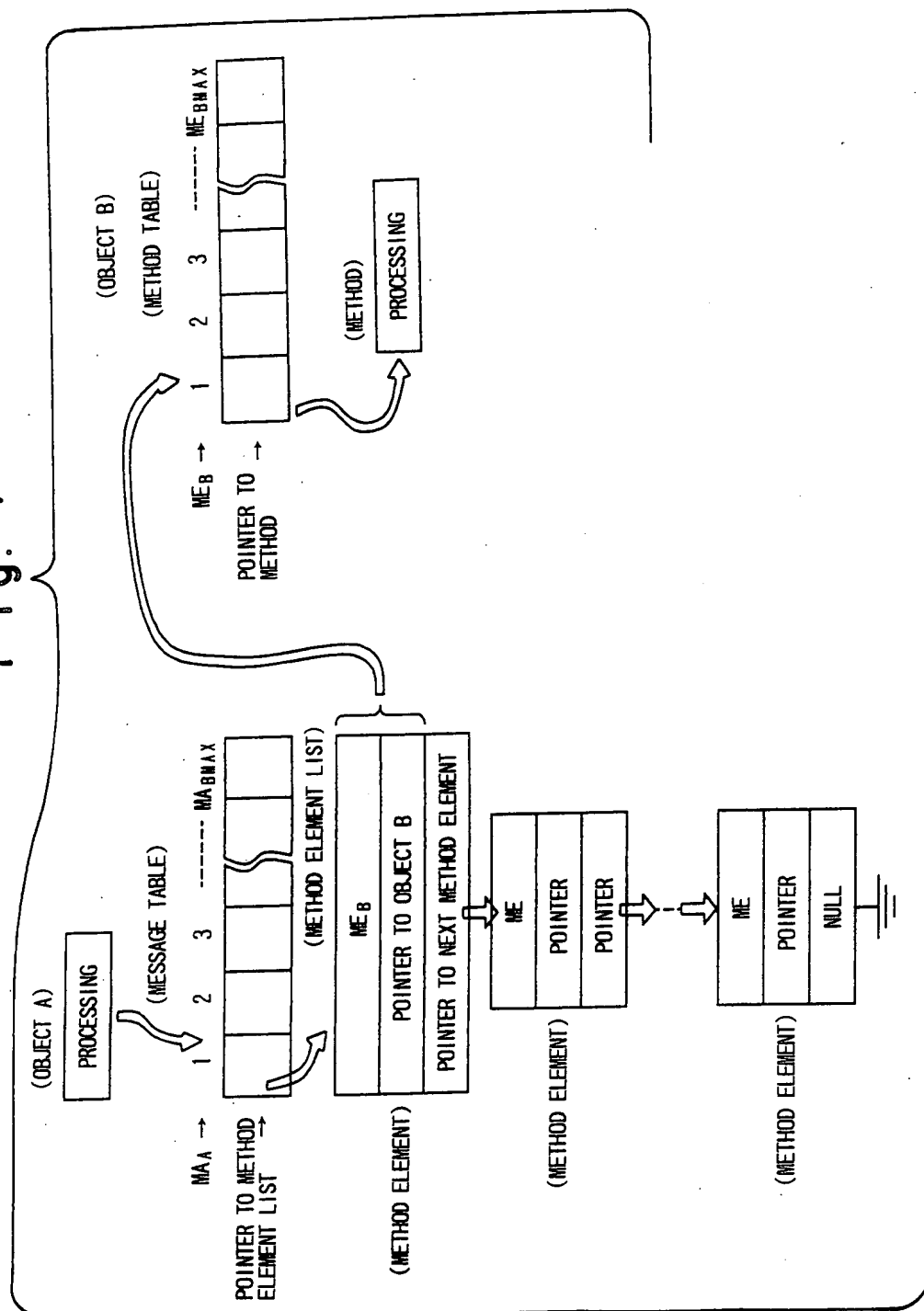


Fig. 5

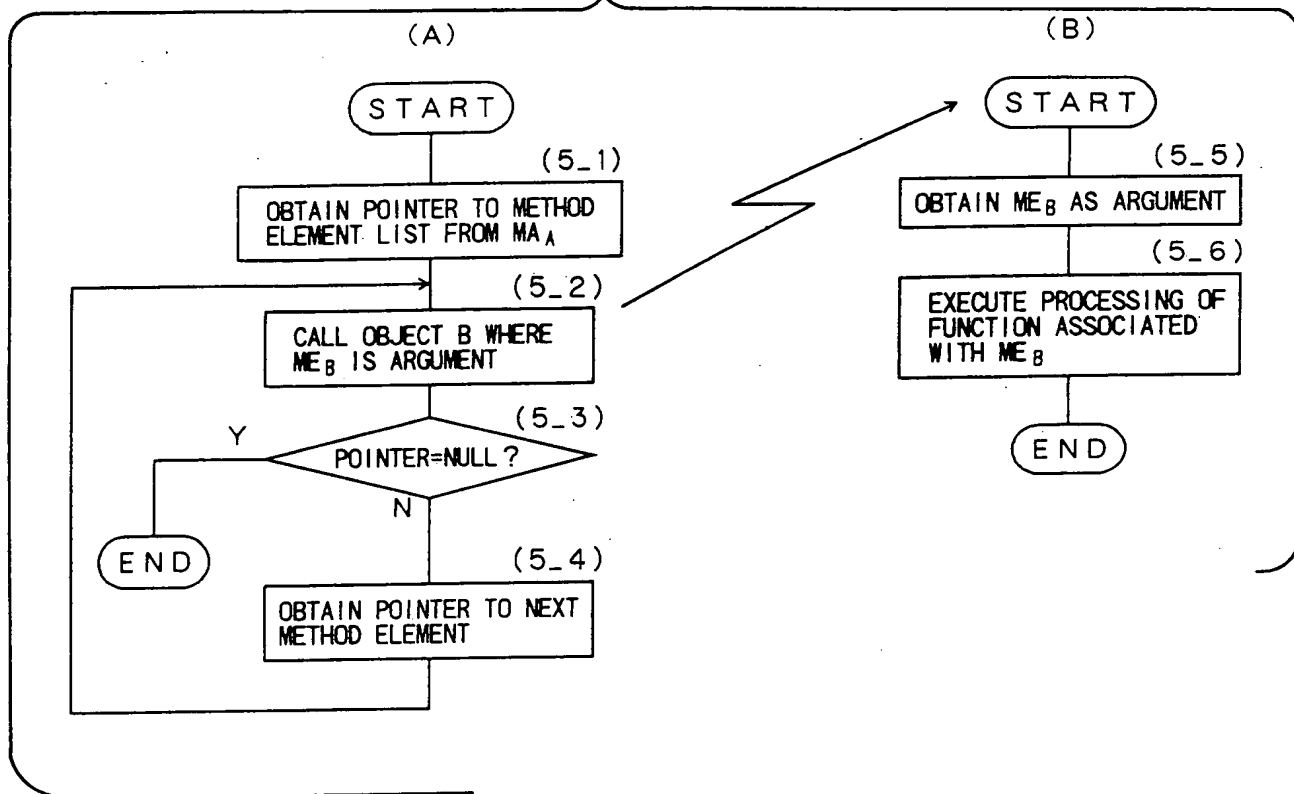


Fig. 6

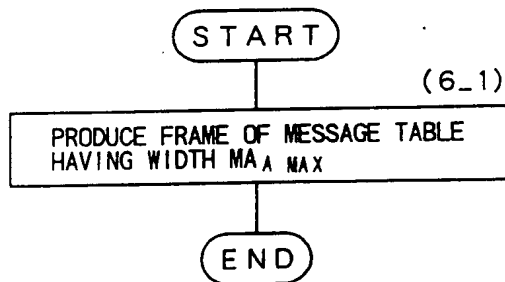


Fig. 7

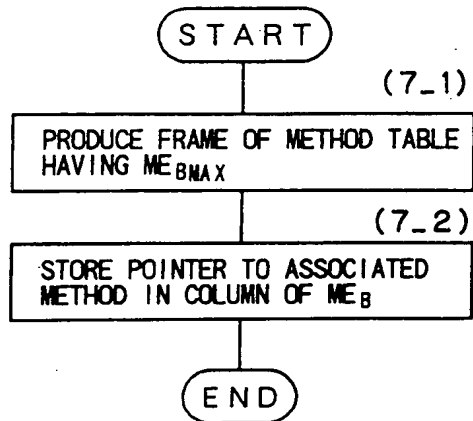


Fig. 8

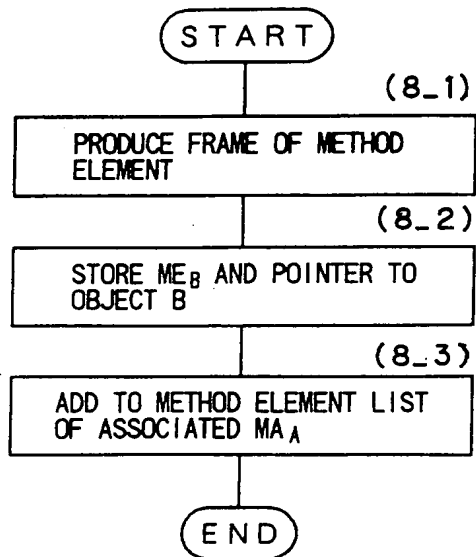


Fig. 9

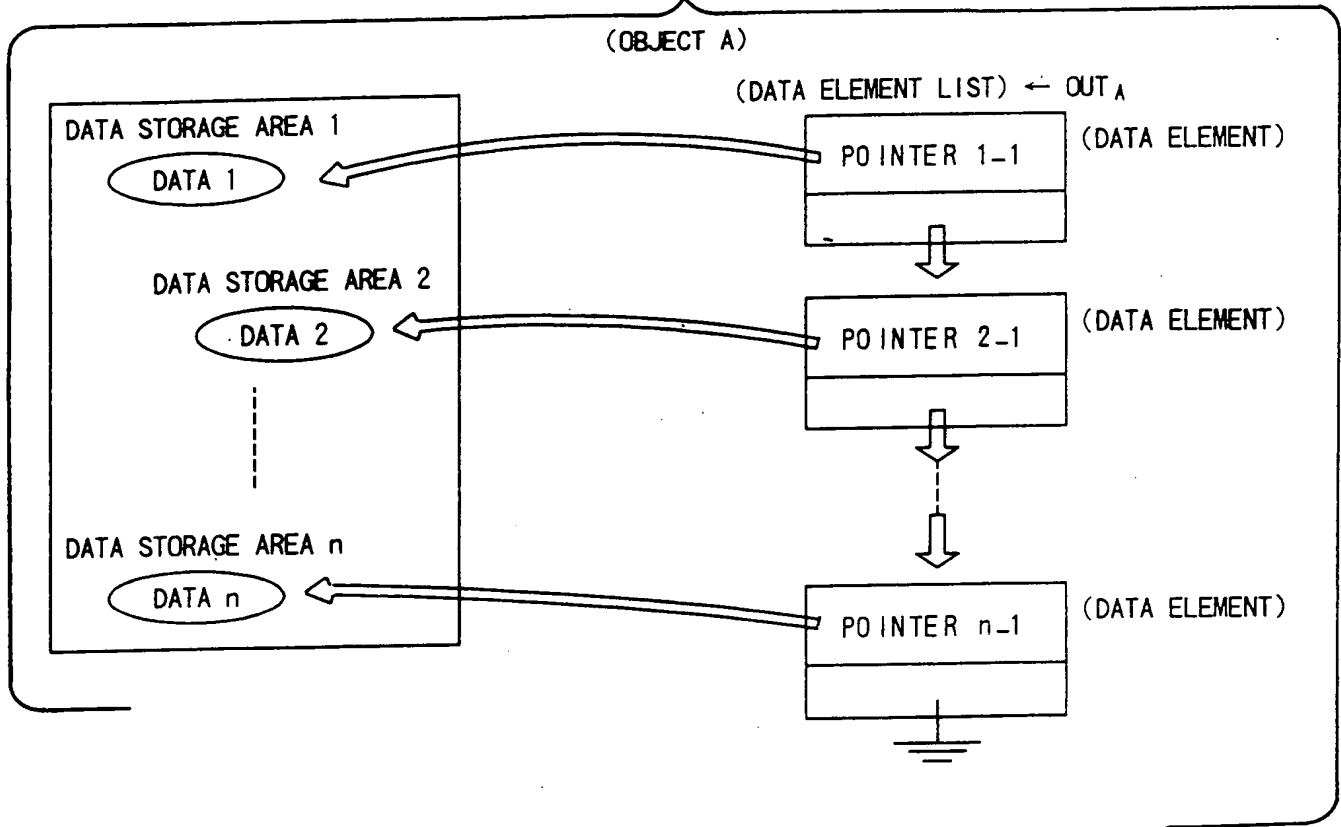


Fig. 10

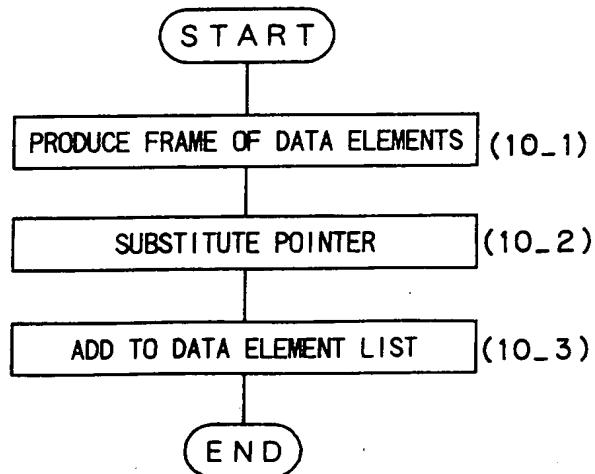
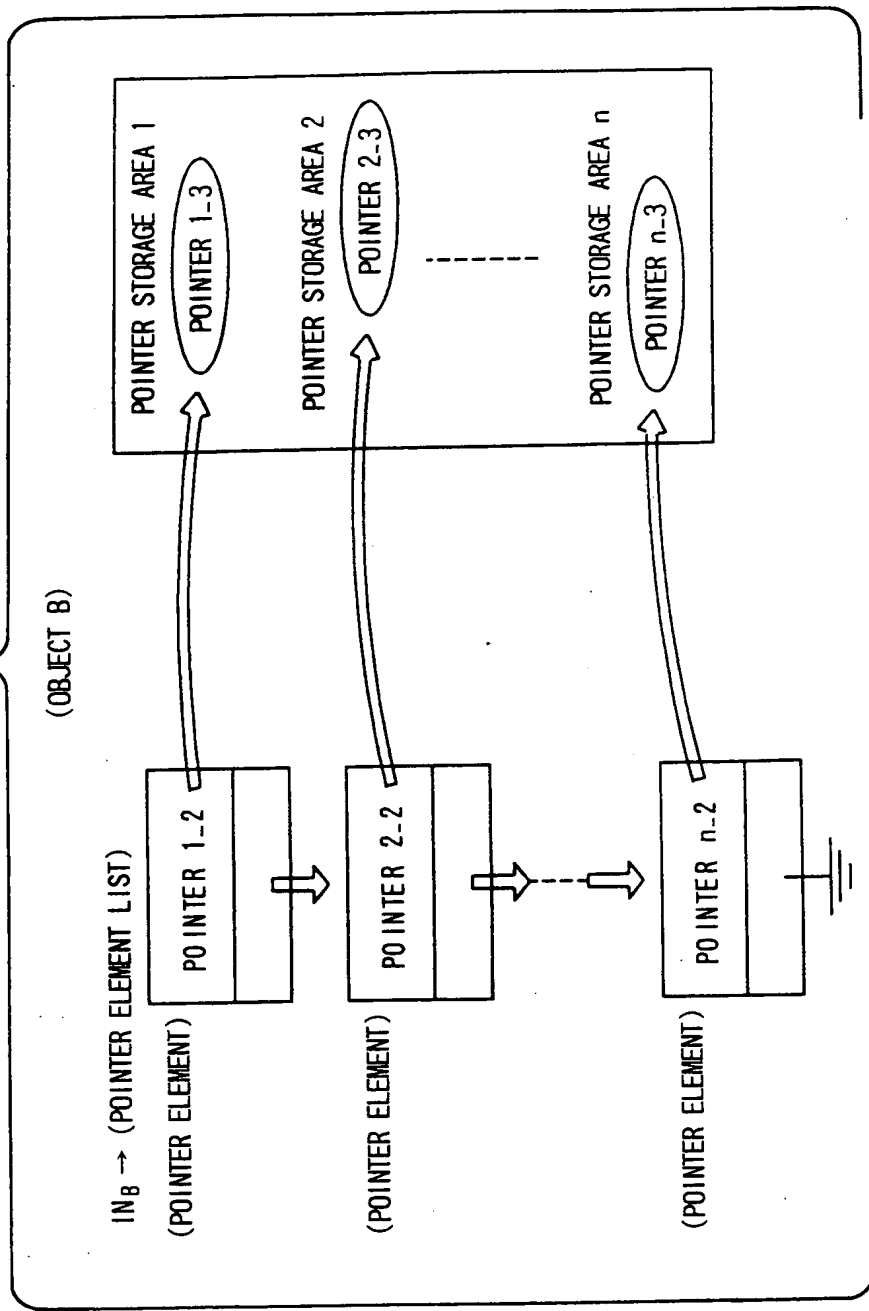


Fig. 11



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Fig. 12

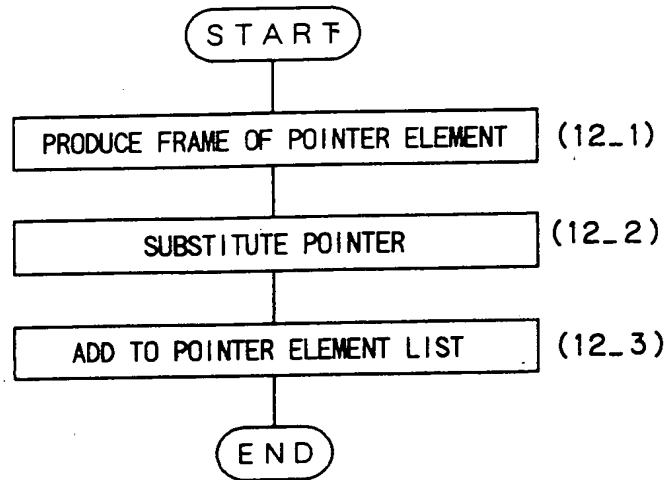


Fig. 13

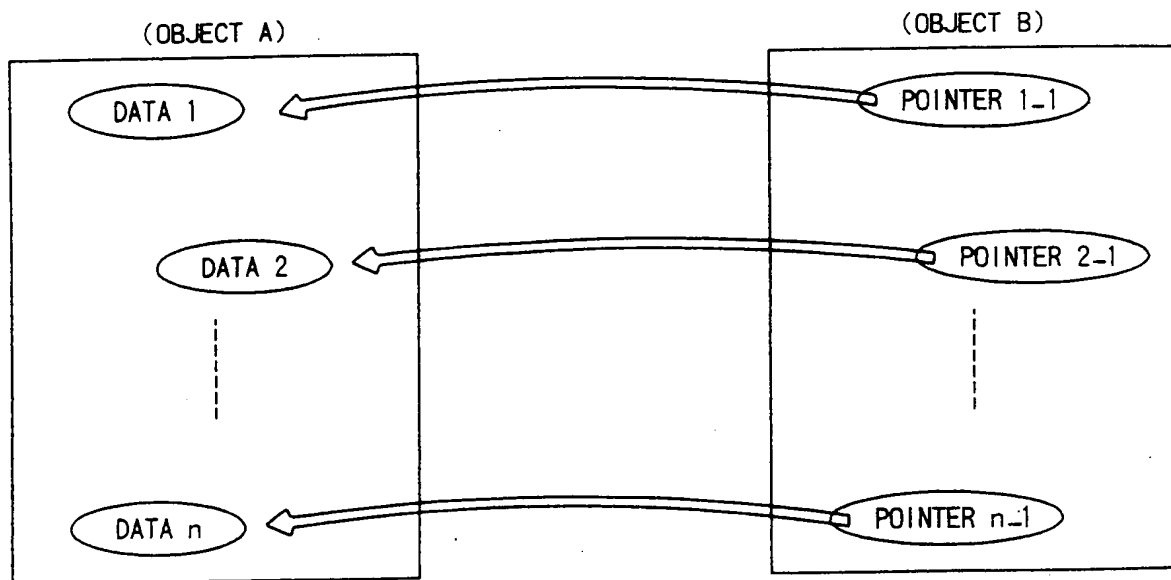
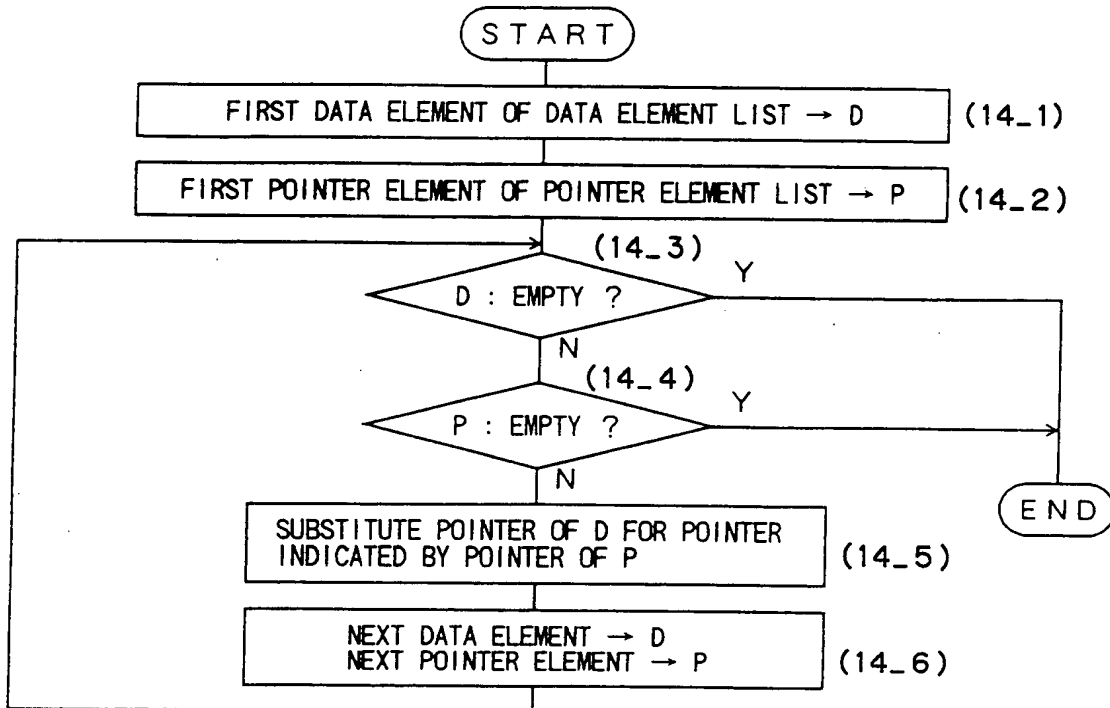


Fig. 14



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Fig. 15

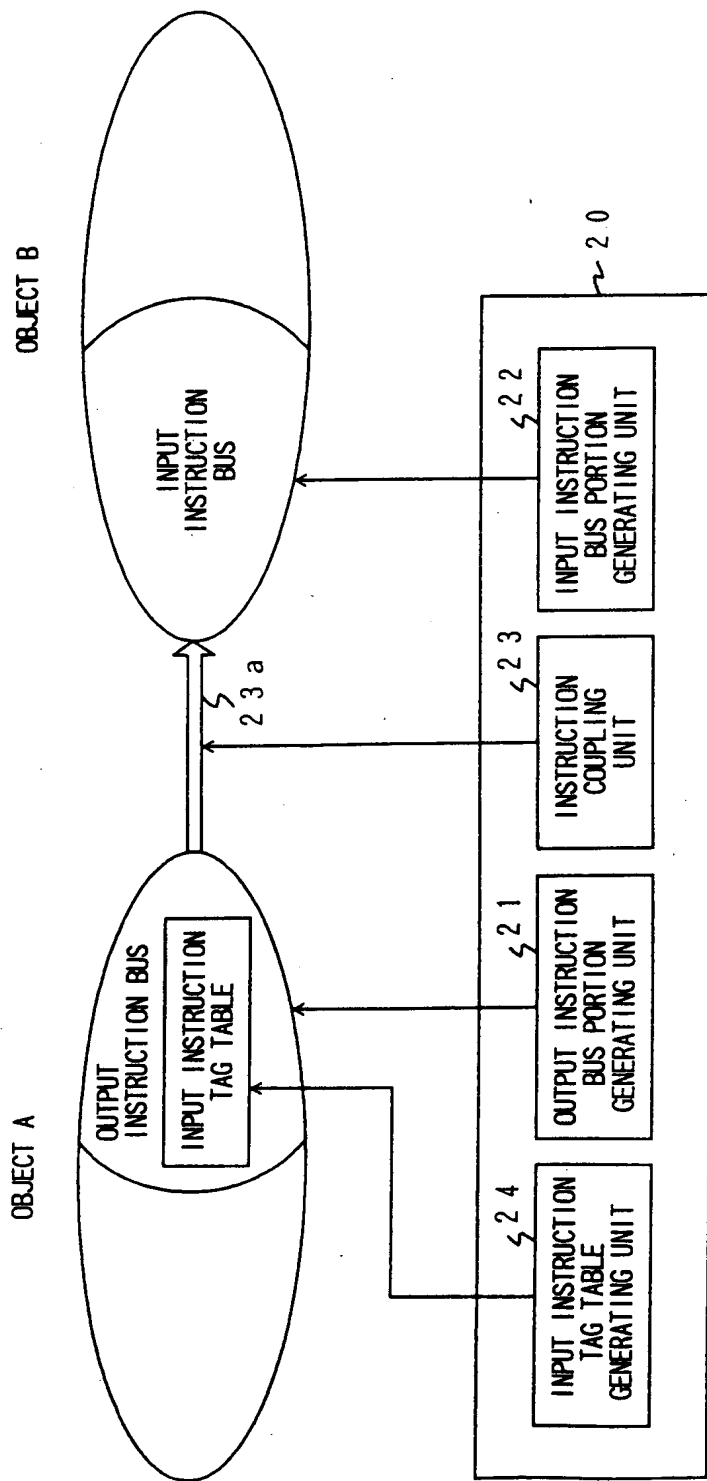


Fig. 16

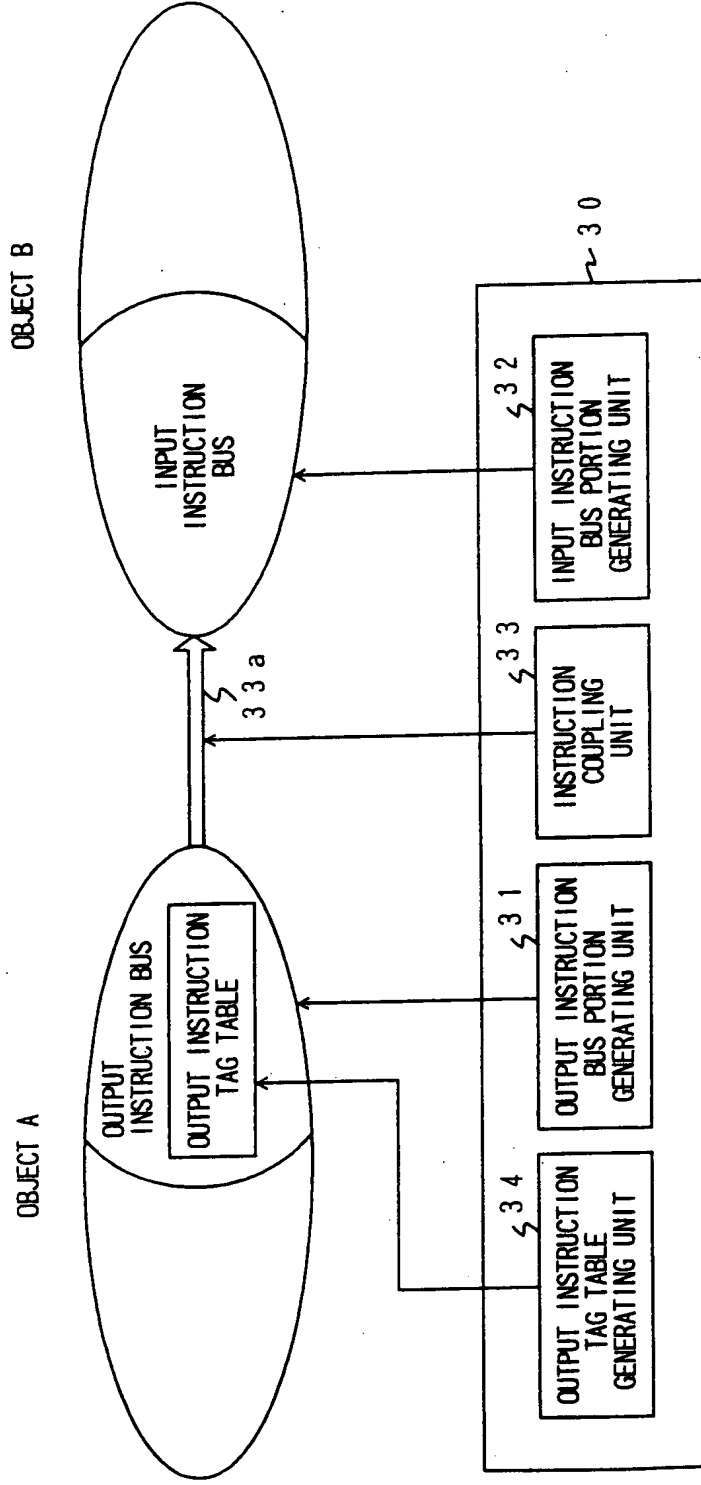


Fig. 17

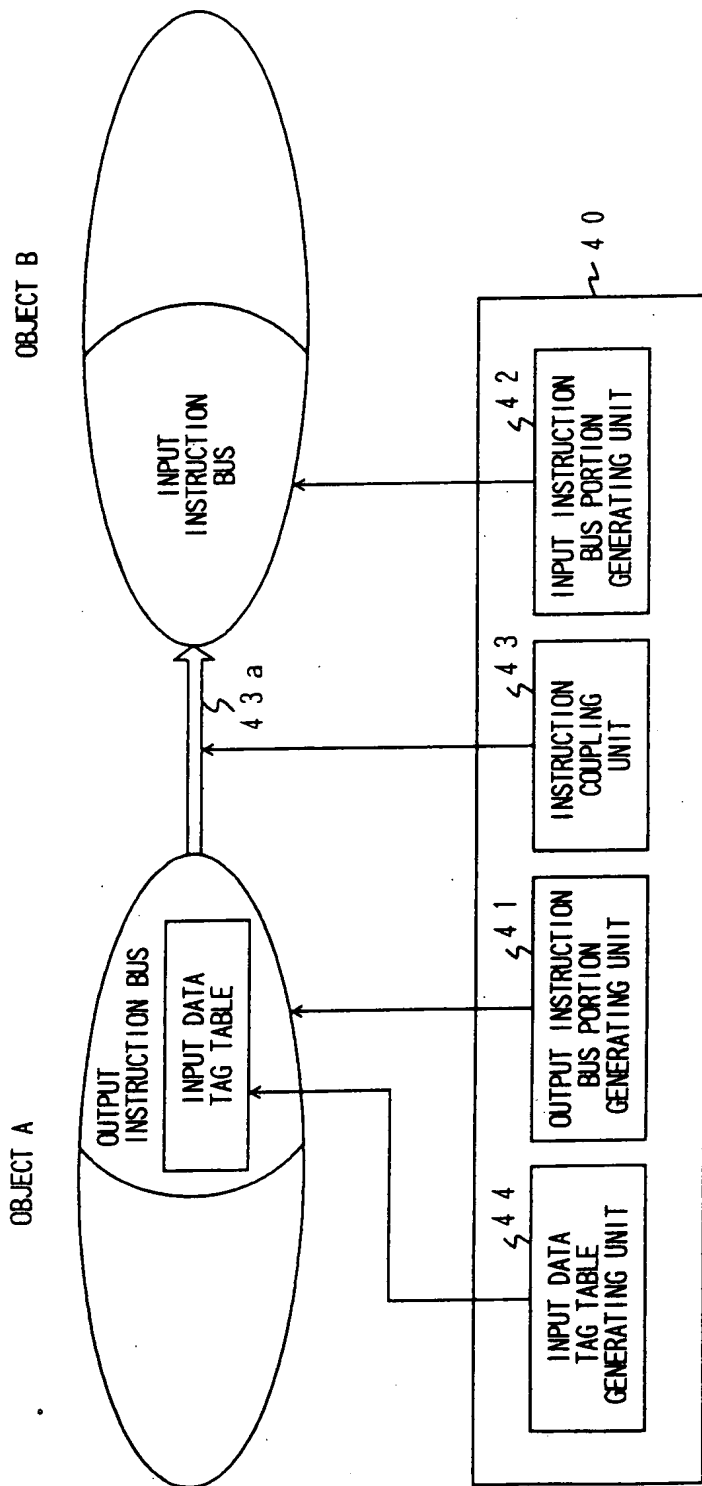


Fig. 18

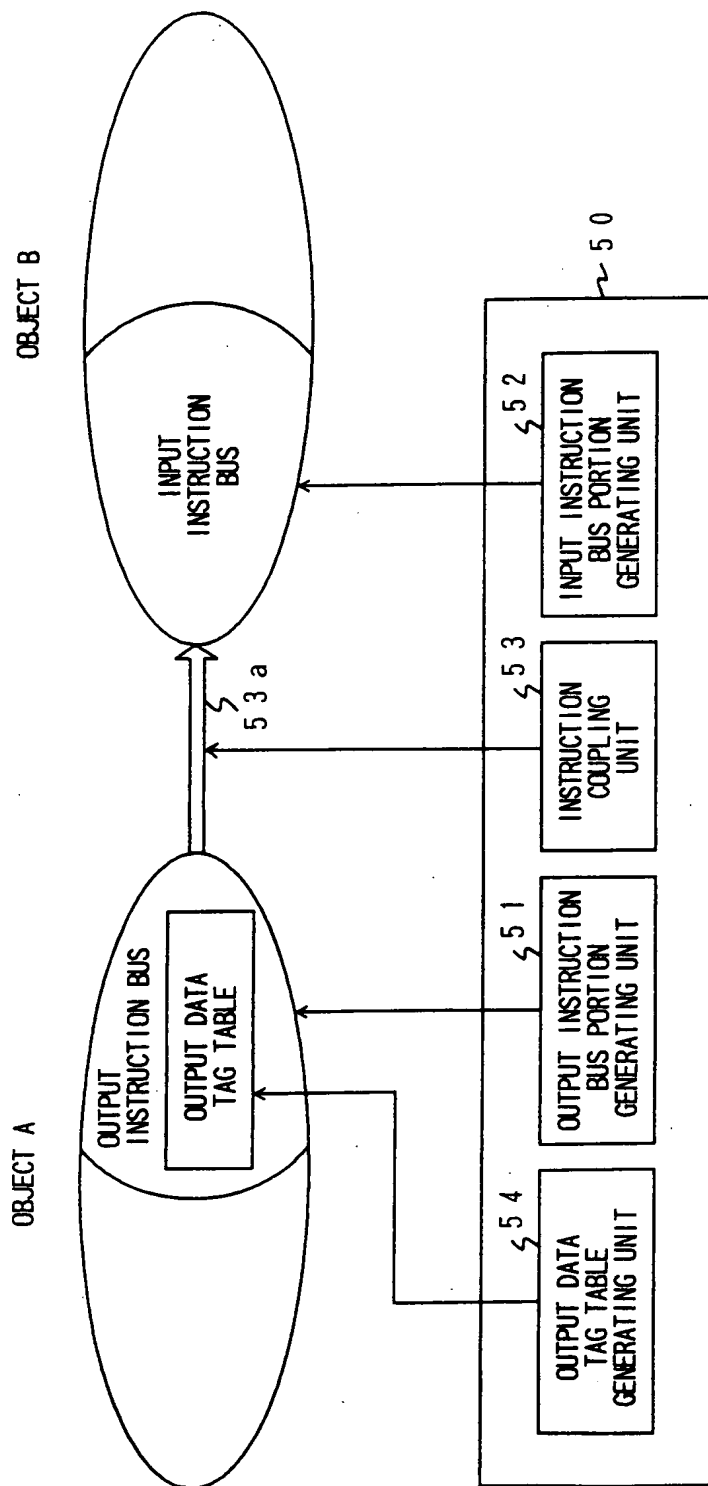


Fig. 19

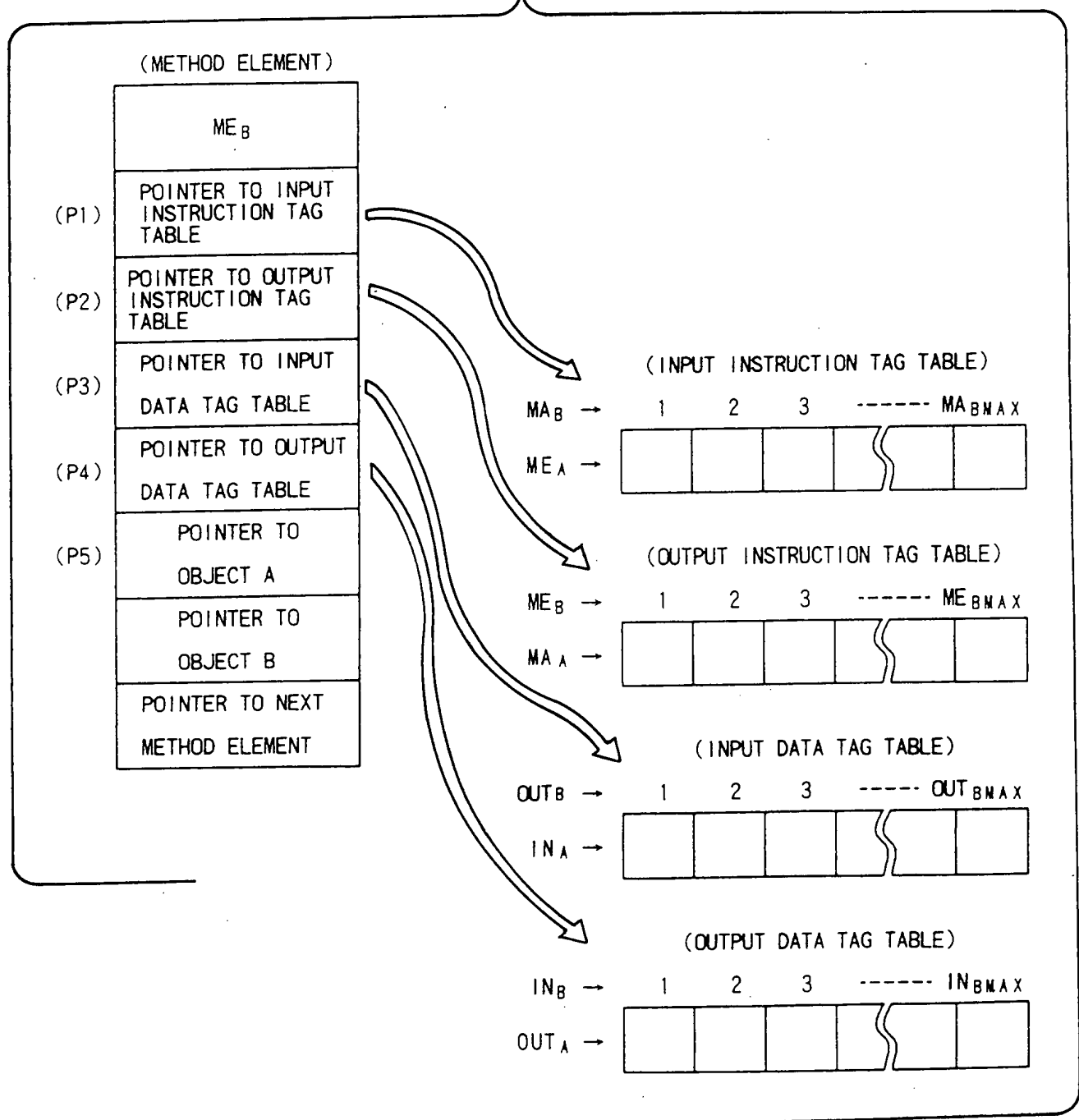


Fig. 20

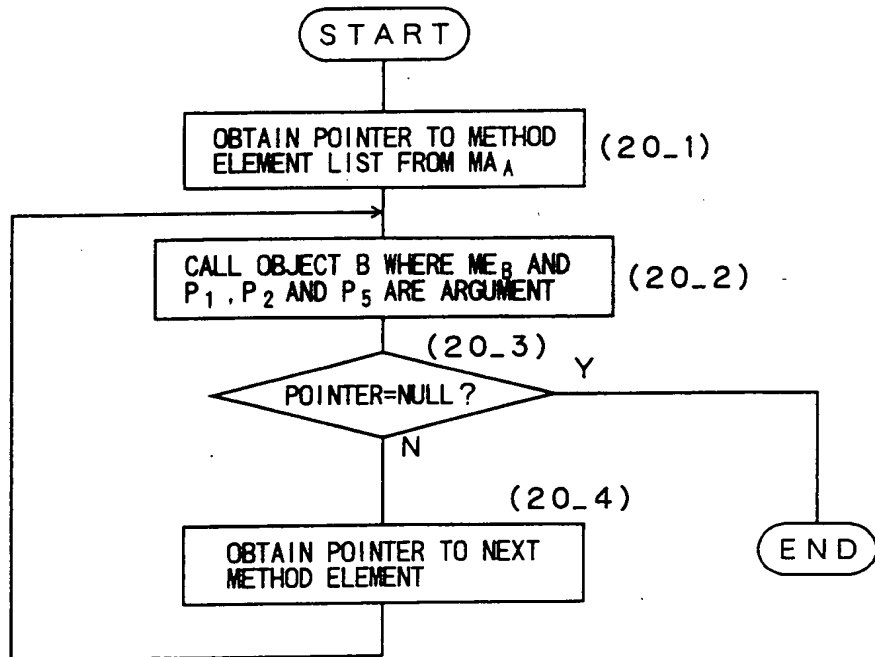
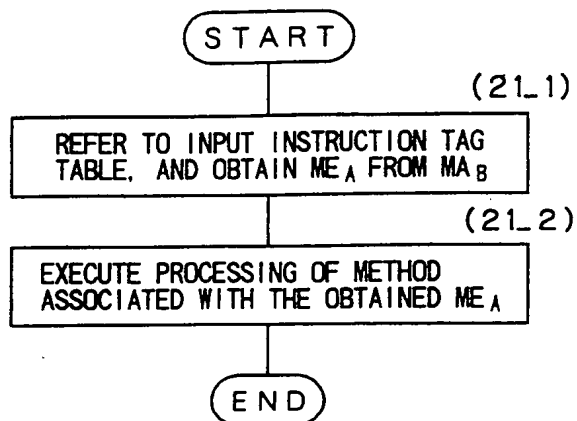


Fig. 21



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Fig.22

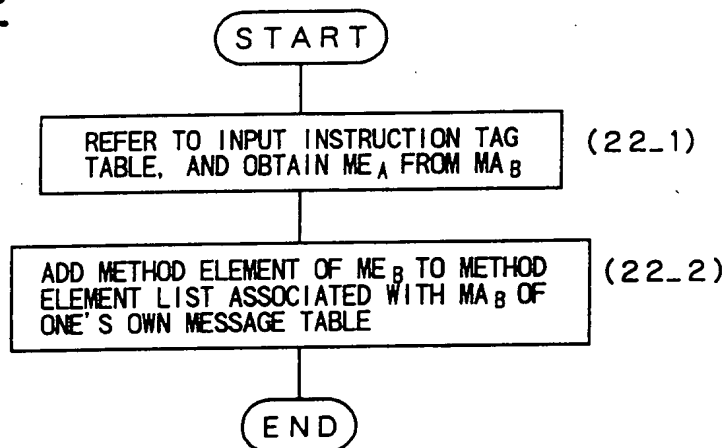


Fig.23

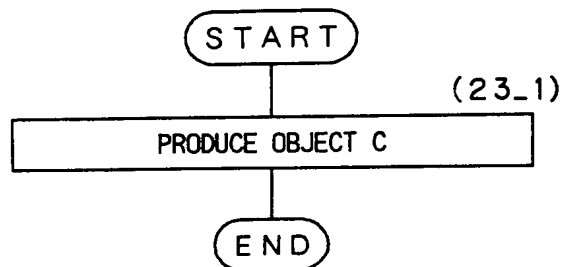
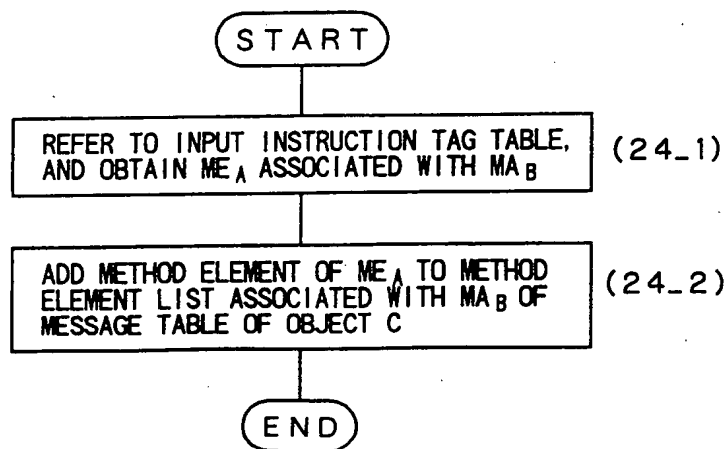


Fig. 24



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Fig. 25

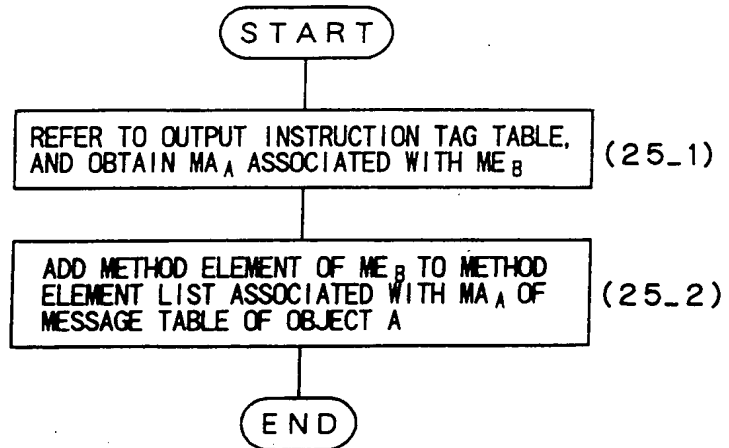
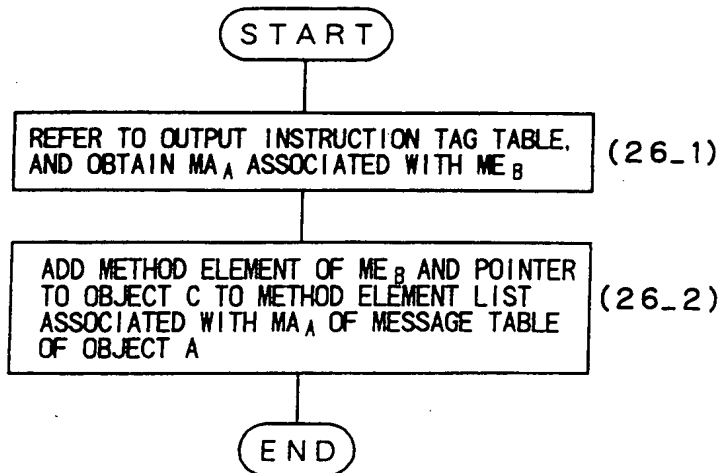


Fig. 26



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Fig. 27

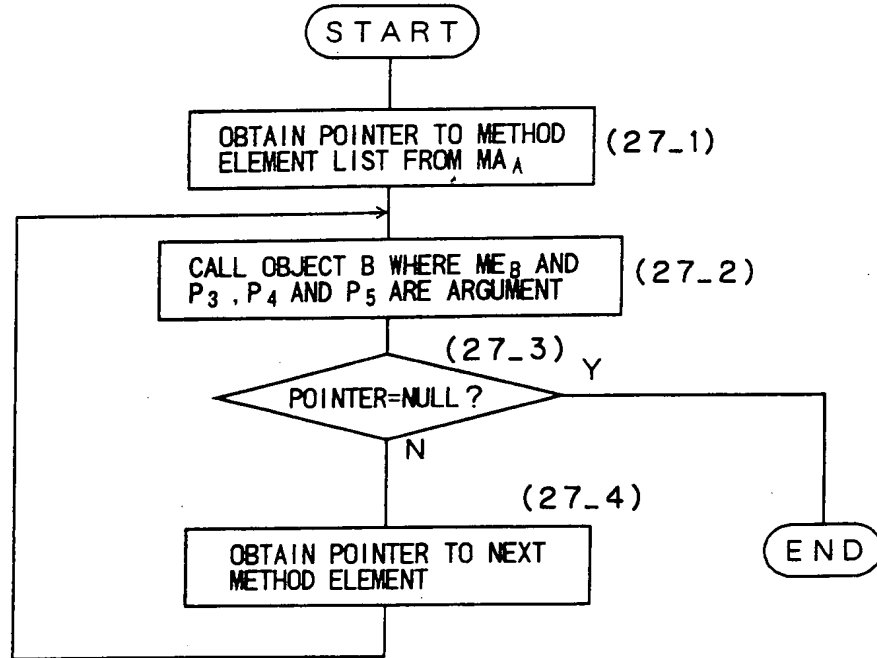


Fig. 28

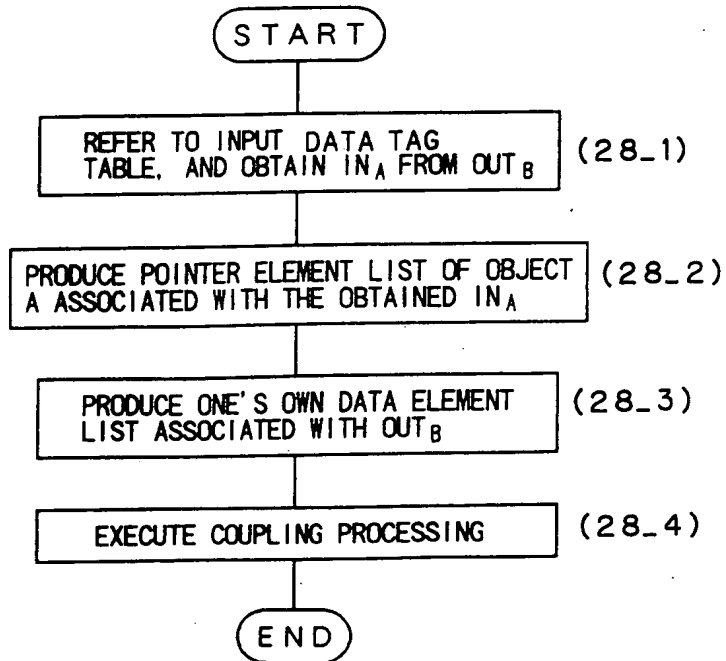


Fig. 29

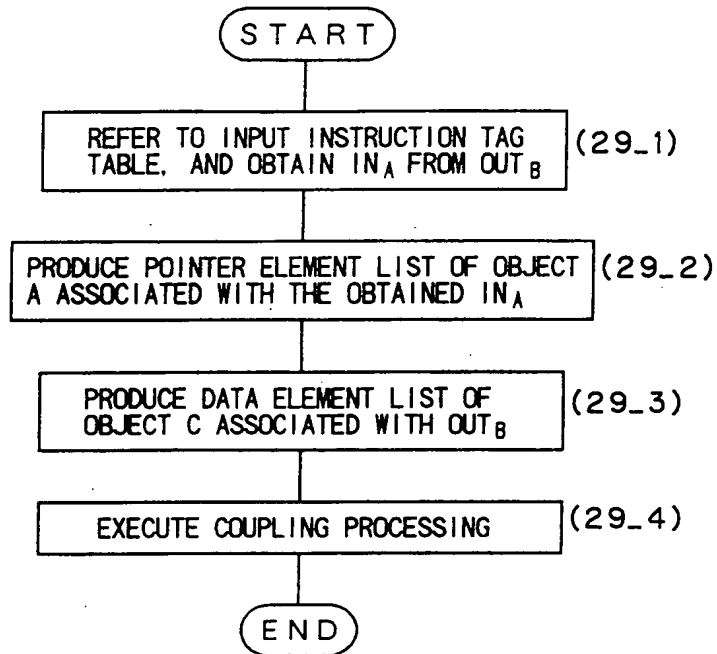


Fig. 30

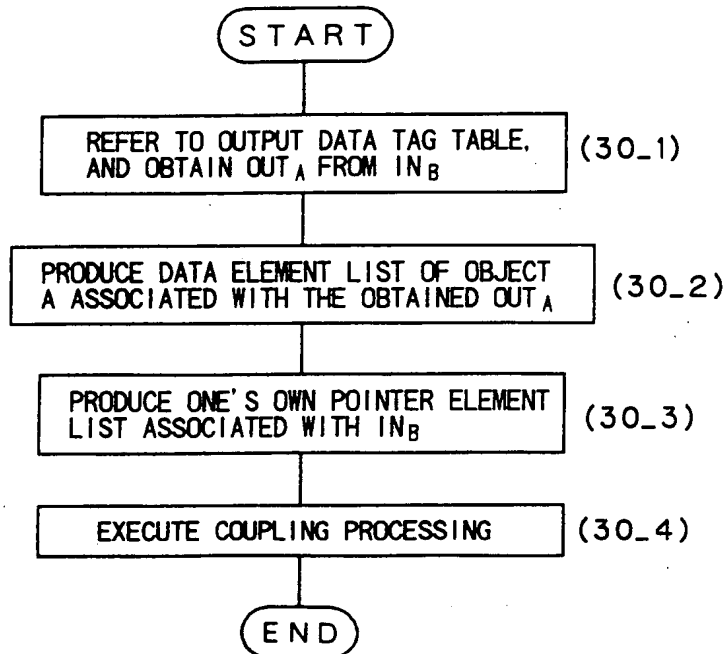


Fig. 31

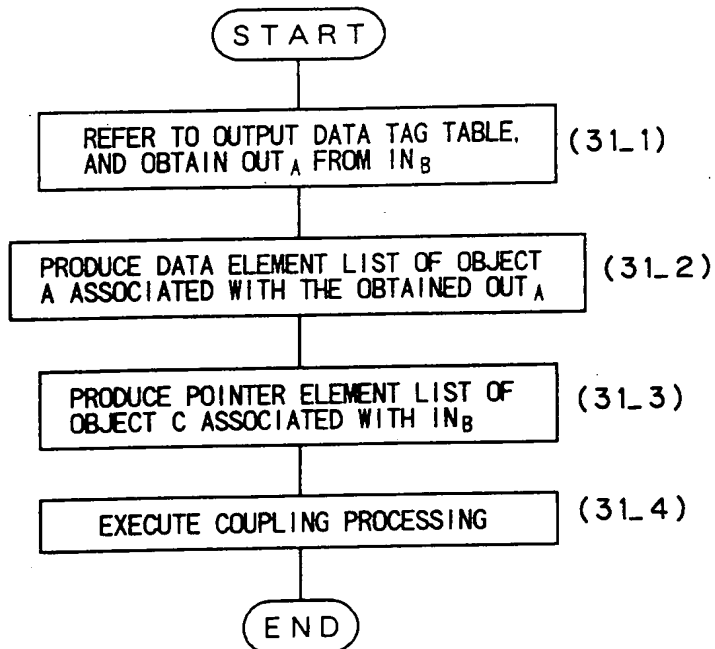


Fig. 32

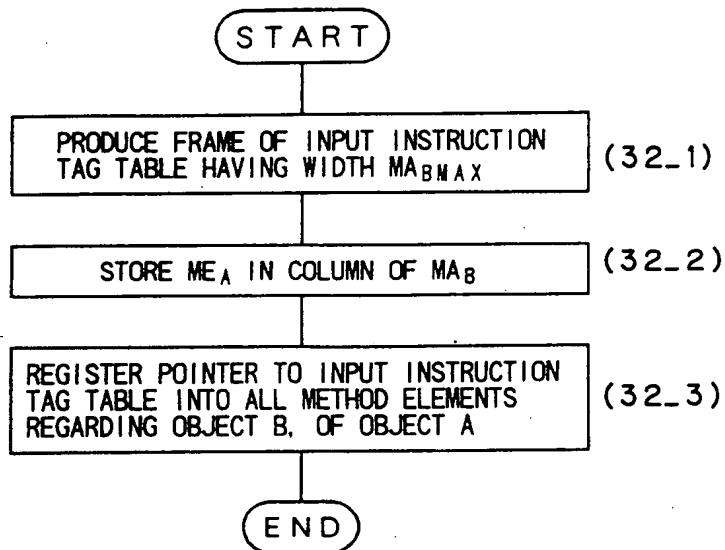


Fig. 33

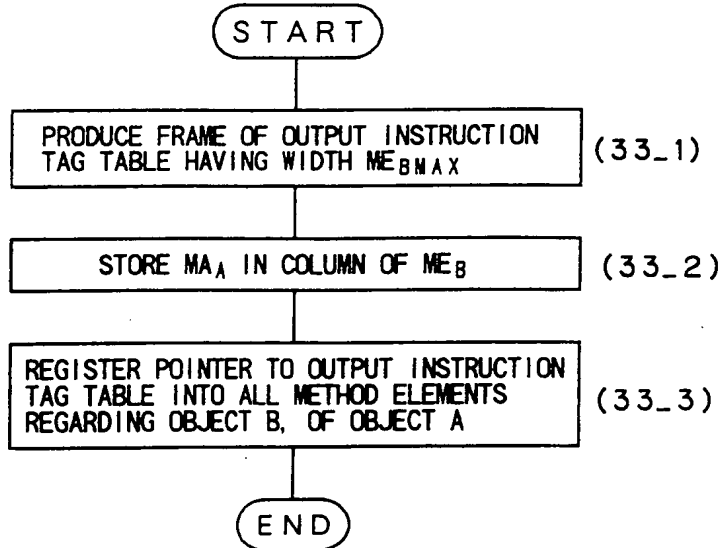


Fig. 34

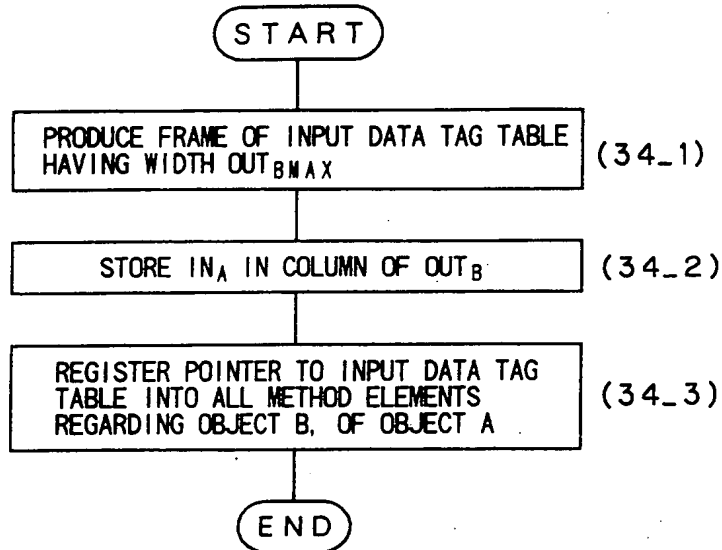
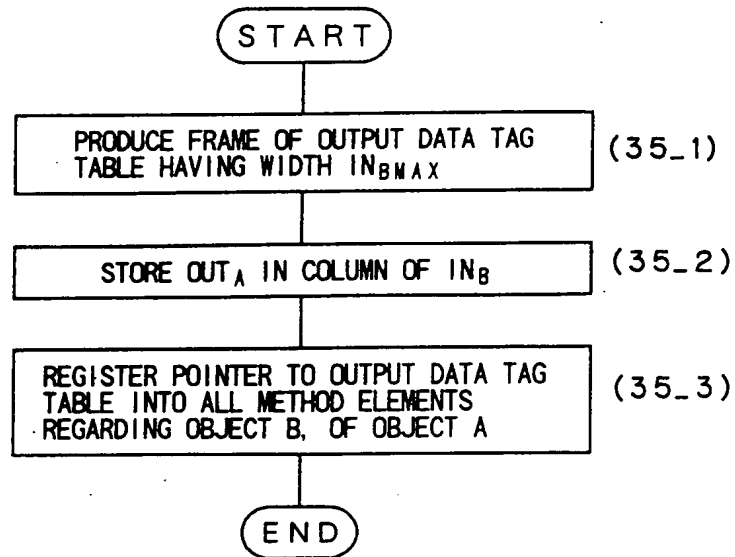


Fig. 35



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Fig. 36

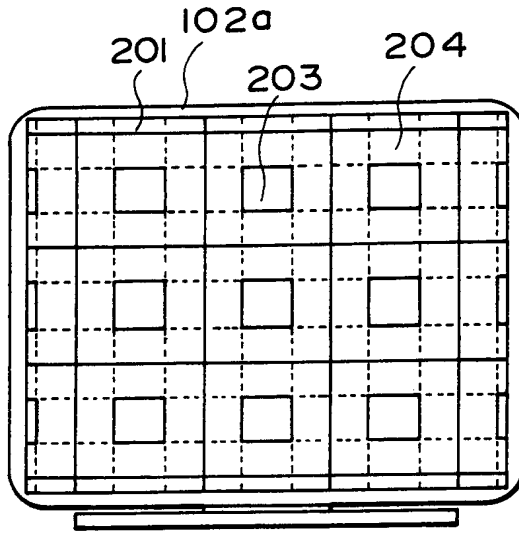
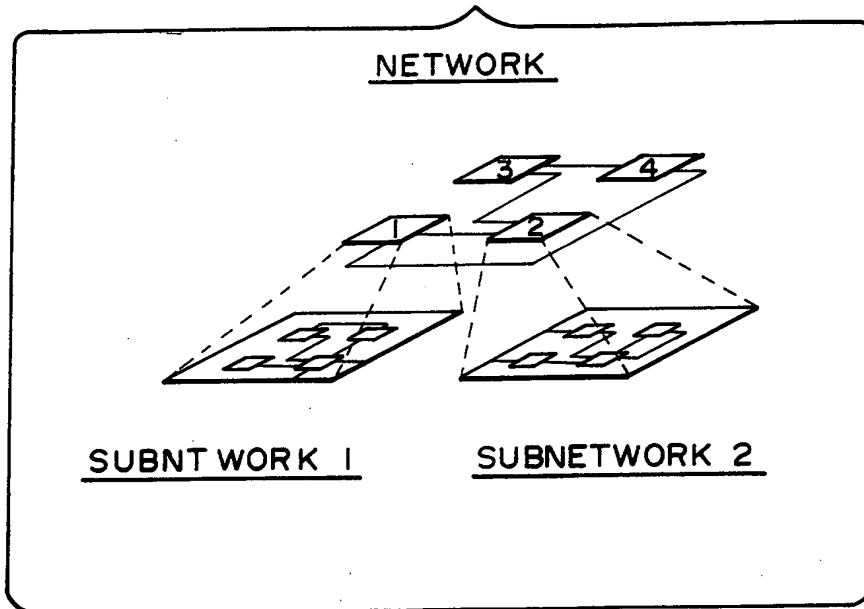
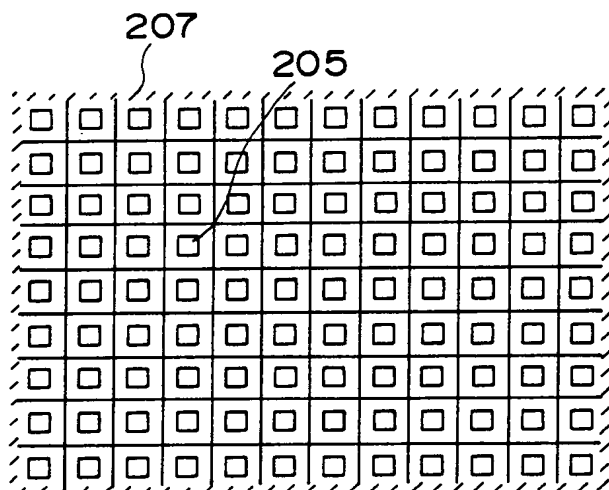


Fig. 37



F i g. 38 (A)



F i g. 38 (B)

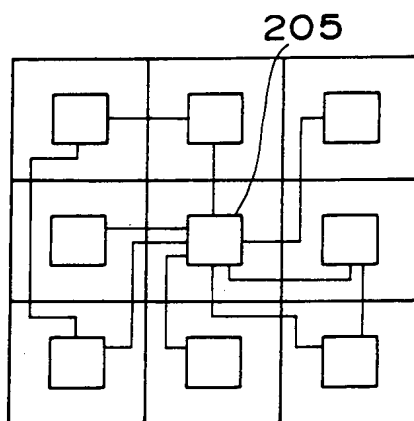


Fig. 39 (A)

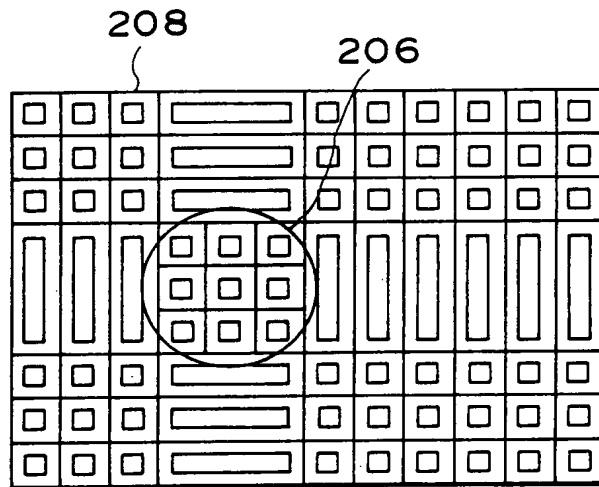


Fig. 39 (B)

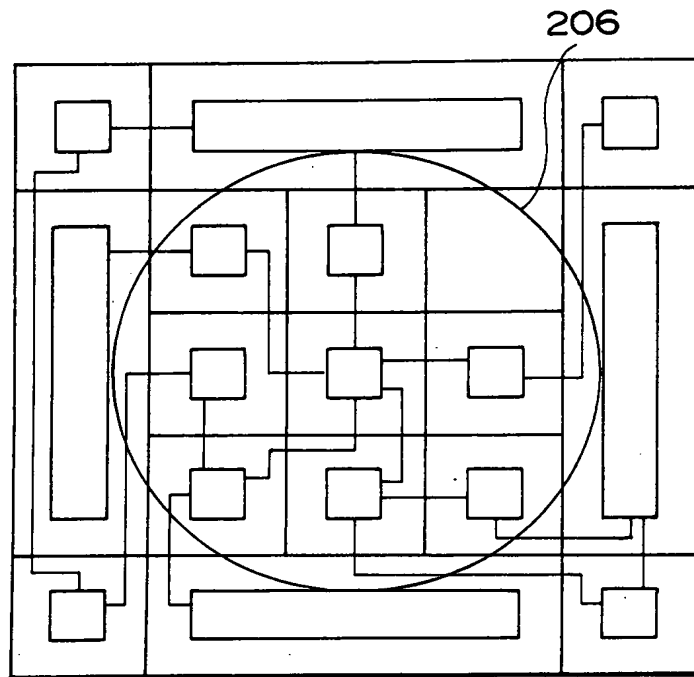


Fig. 40 (A)

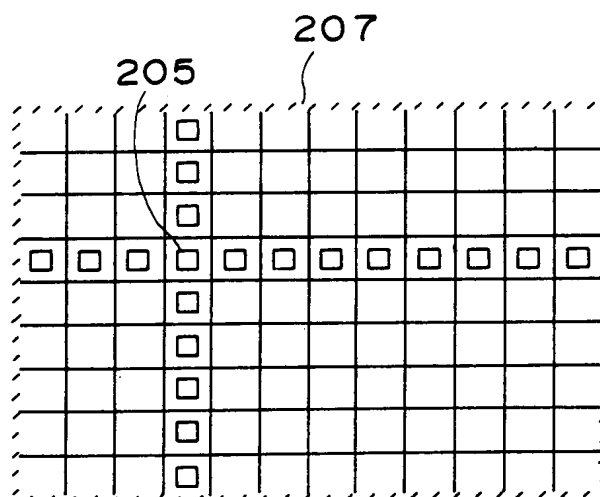


Fig. 40 (B)

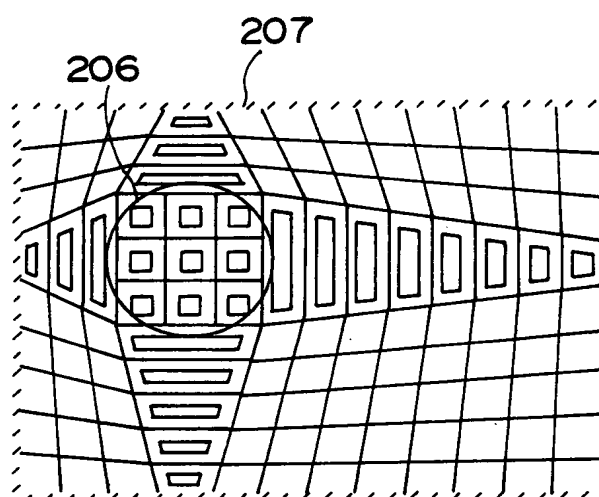


Fig. 41(B)

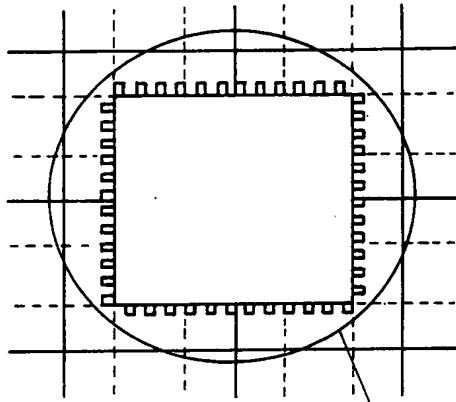


Fig. 41(C)

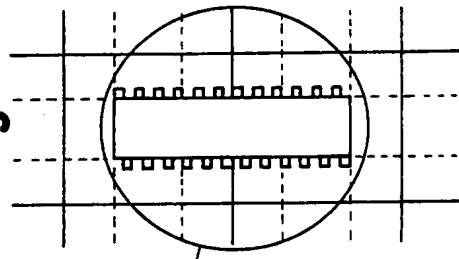


Fig. 41(A)

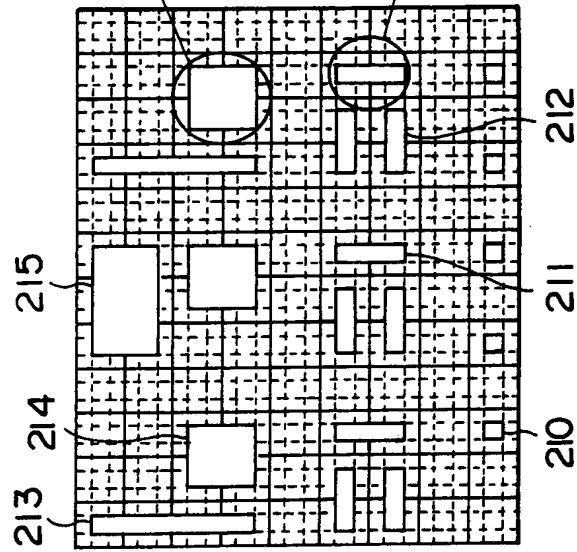
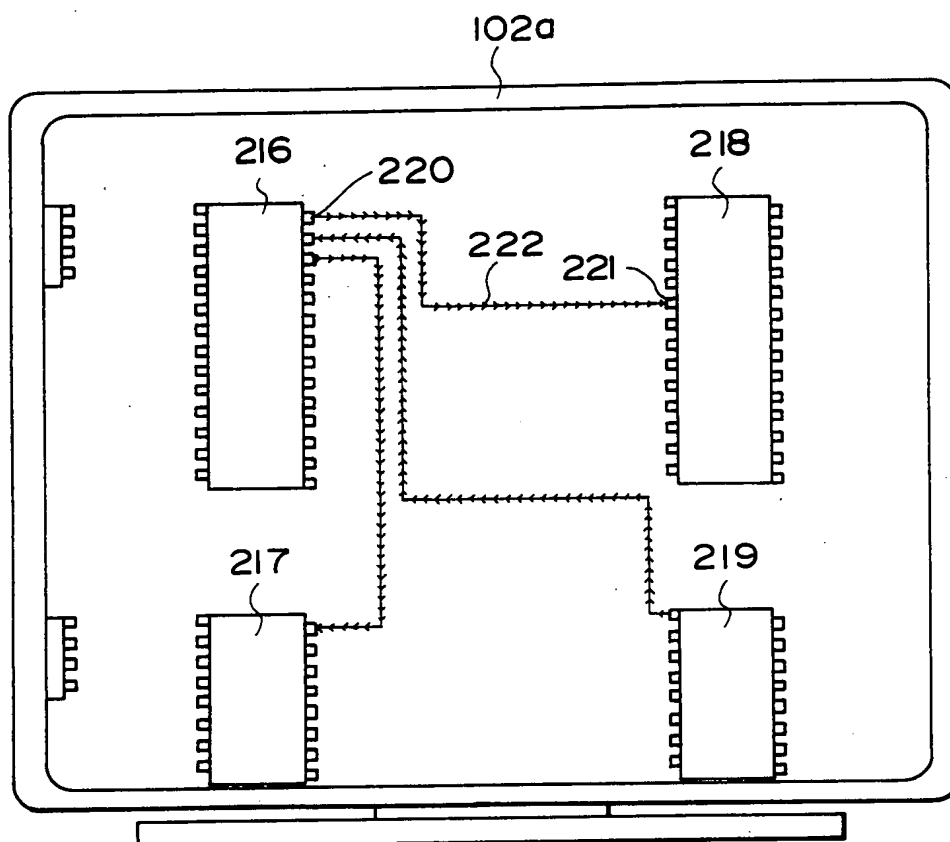


Fig. 42



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Fig. 43(A)

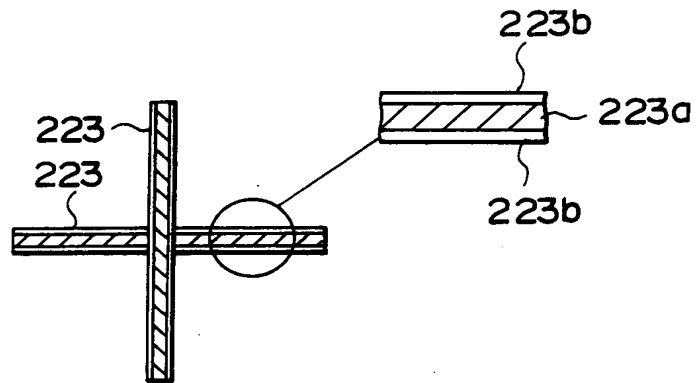


Fig. 43(B)

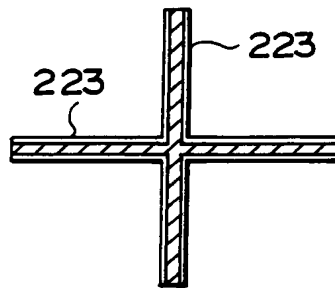


Fig. 44 (A)

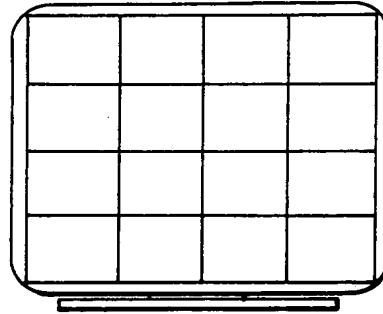


Fig. 44 (B)

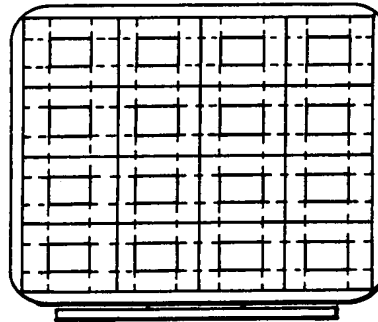


Fig. 44 (C)

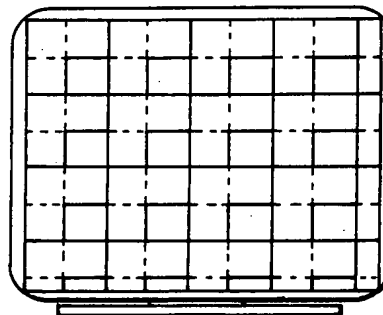


Fig. 45

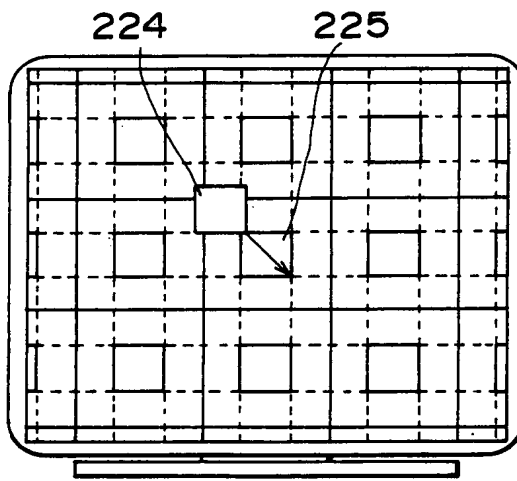


Fig.46(A)

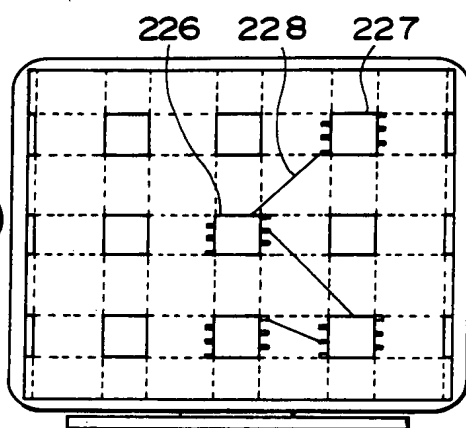
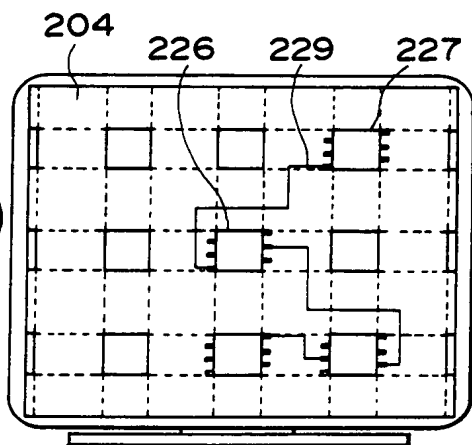


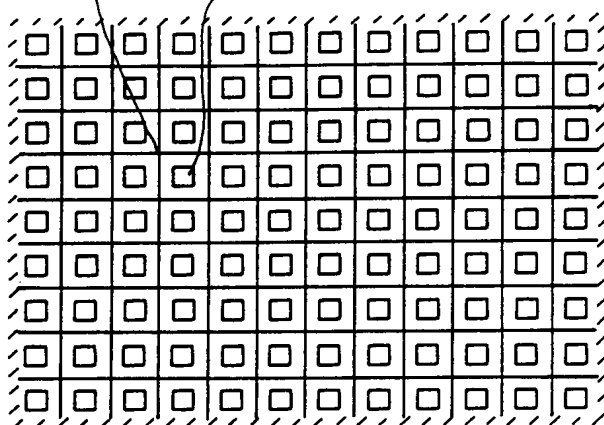
Fig.46(B)



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F i g . 4 7 (A)

STARTING POINT OF ENLARGEMENT
OBJECT HAVING SUBNETWORK



F i g . 4 7 (B)

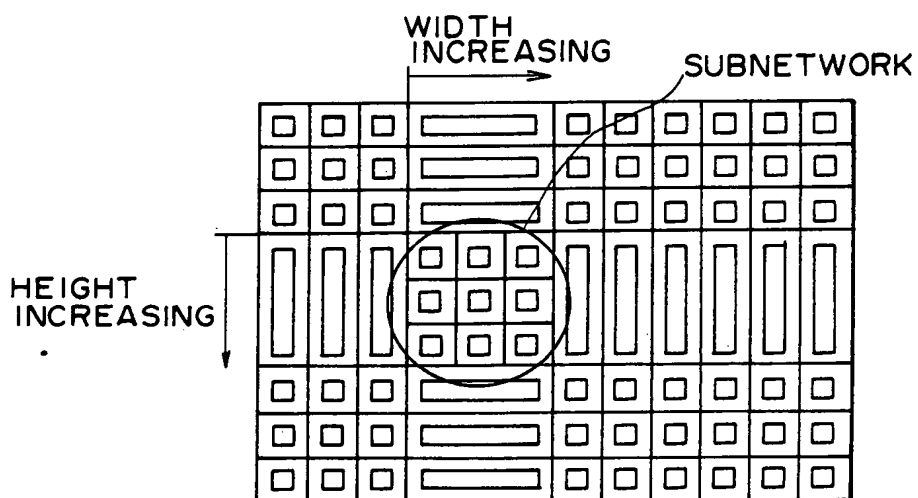
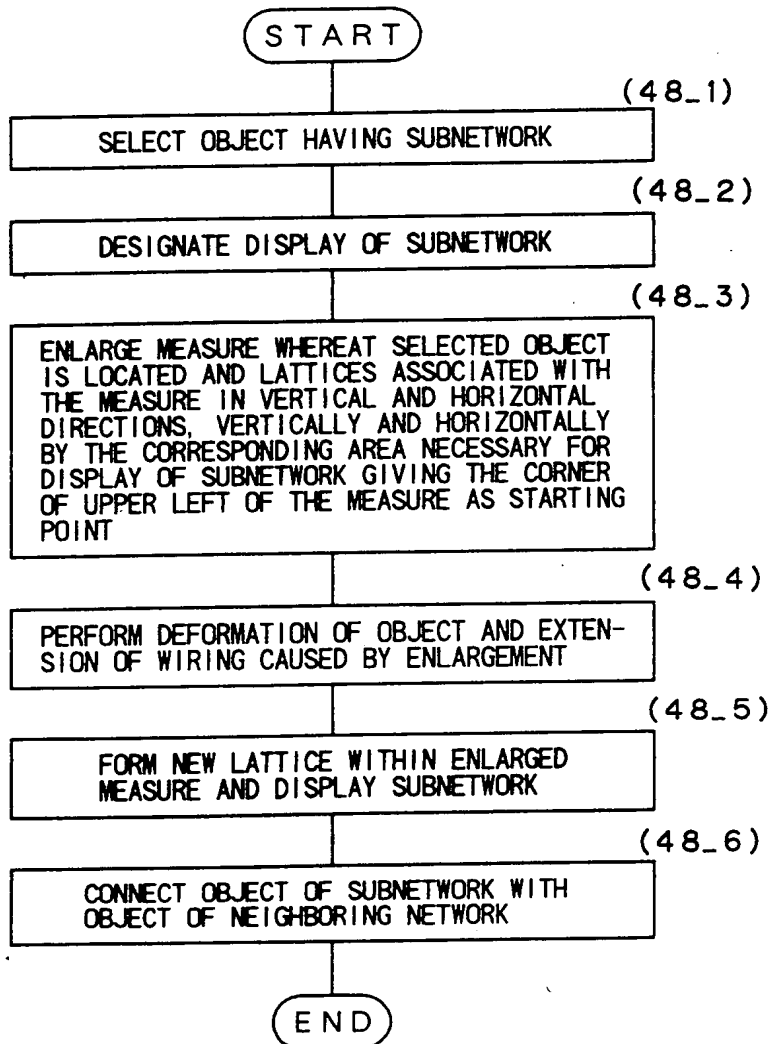


Fig. 48



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Fig.49(A)

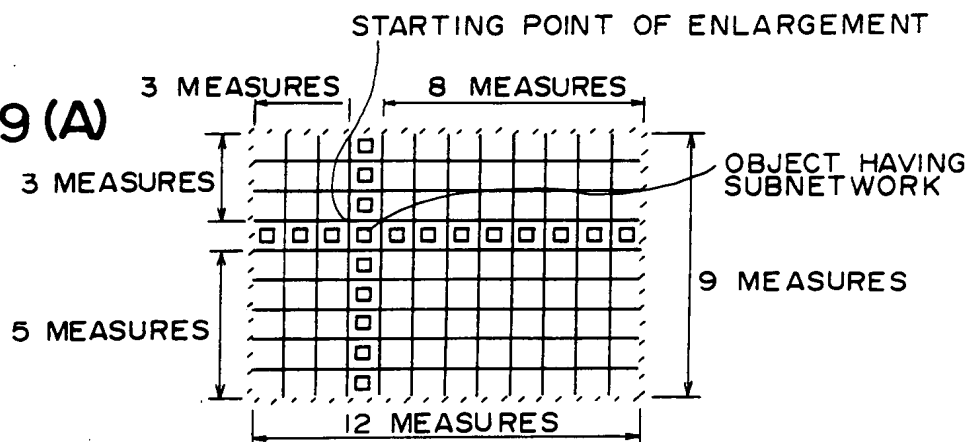


Fig.49(B)

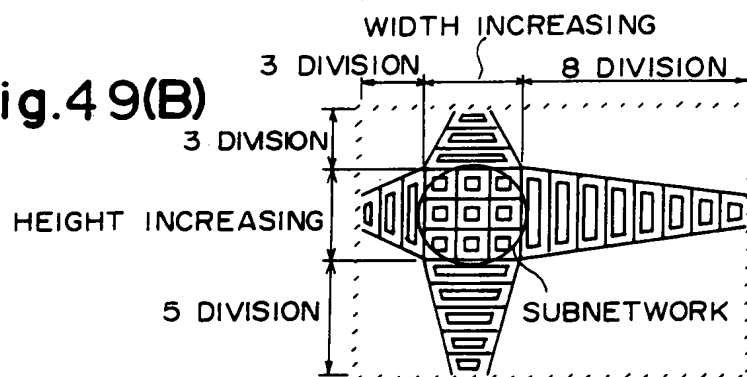


Fig.49(C)

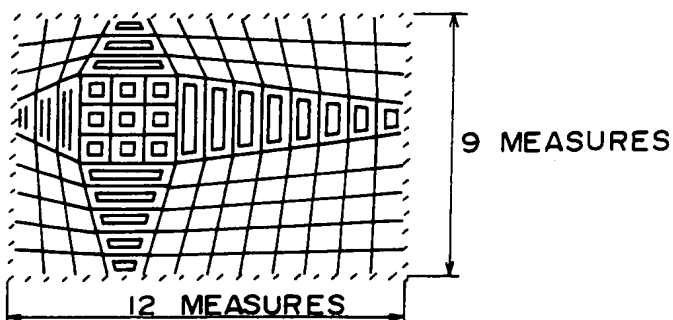


Fig. 50

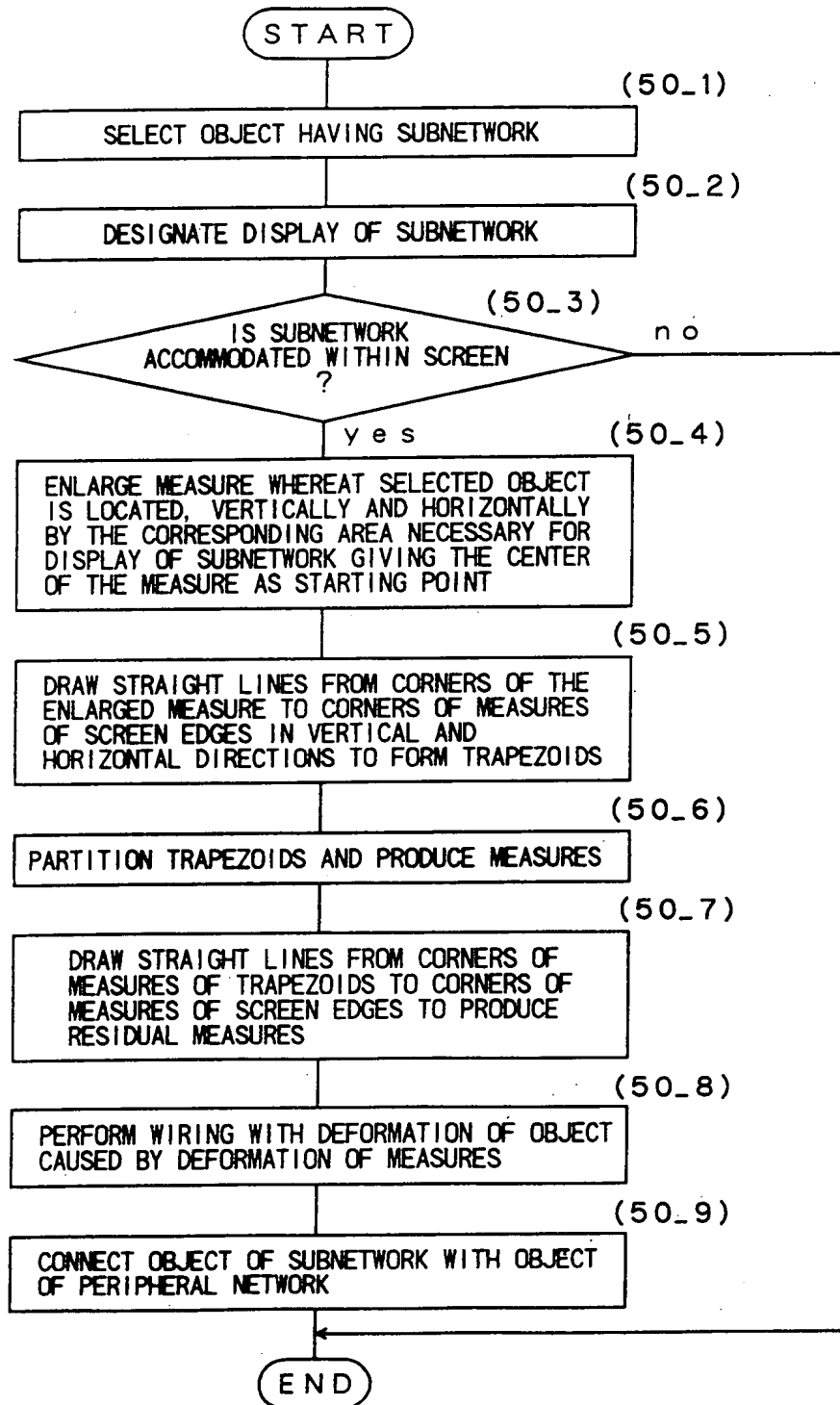


Fig.51(A)

6 TERMINALS 12 TERMINALS

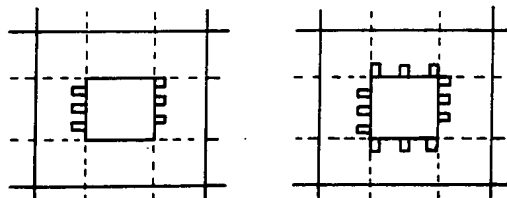


Fig.51(B)

24 TERMINALS 30 TERMINALS

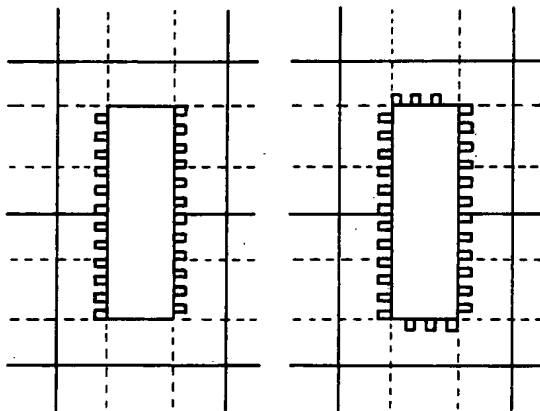


Fig.51(C)

48 TERMINALS

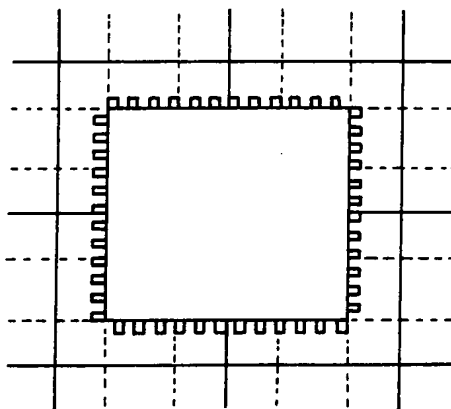


Fig.52(A)

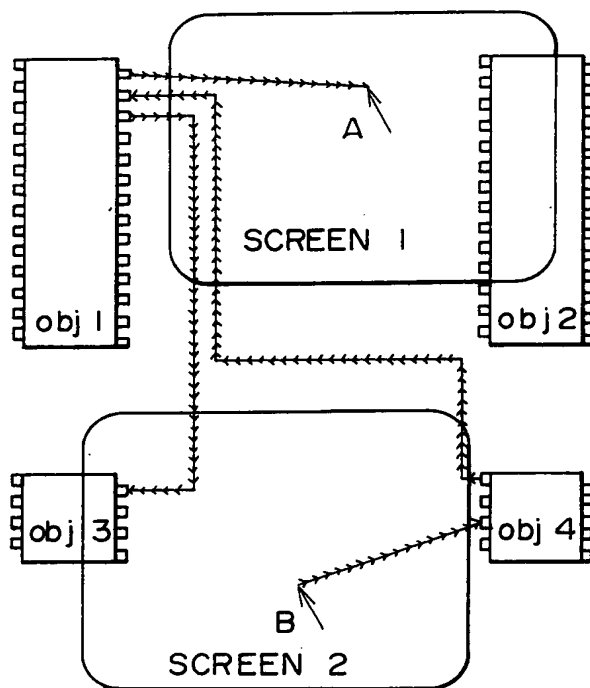


Fig.52(B)

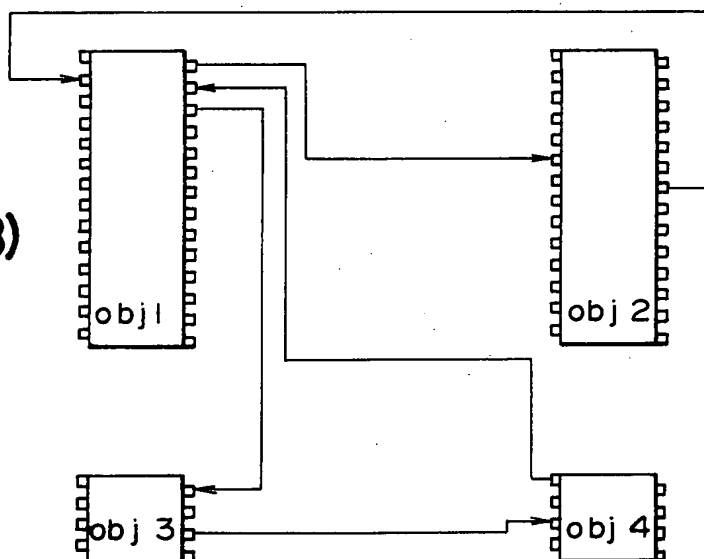


Fig. 53

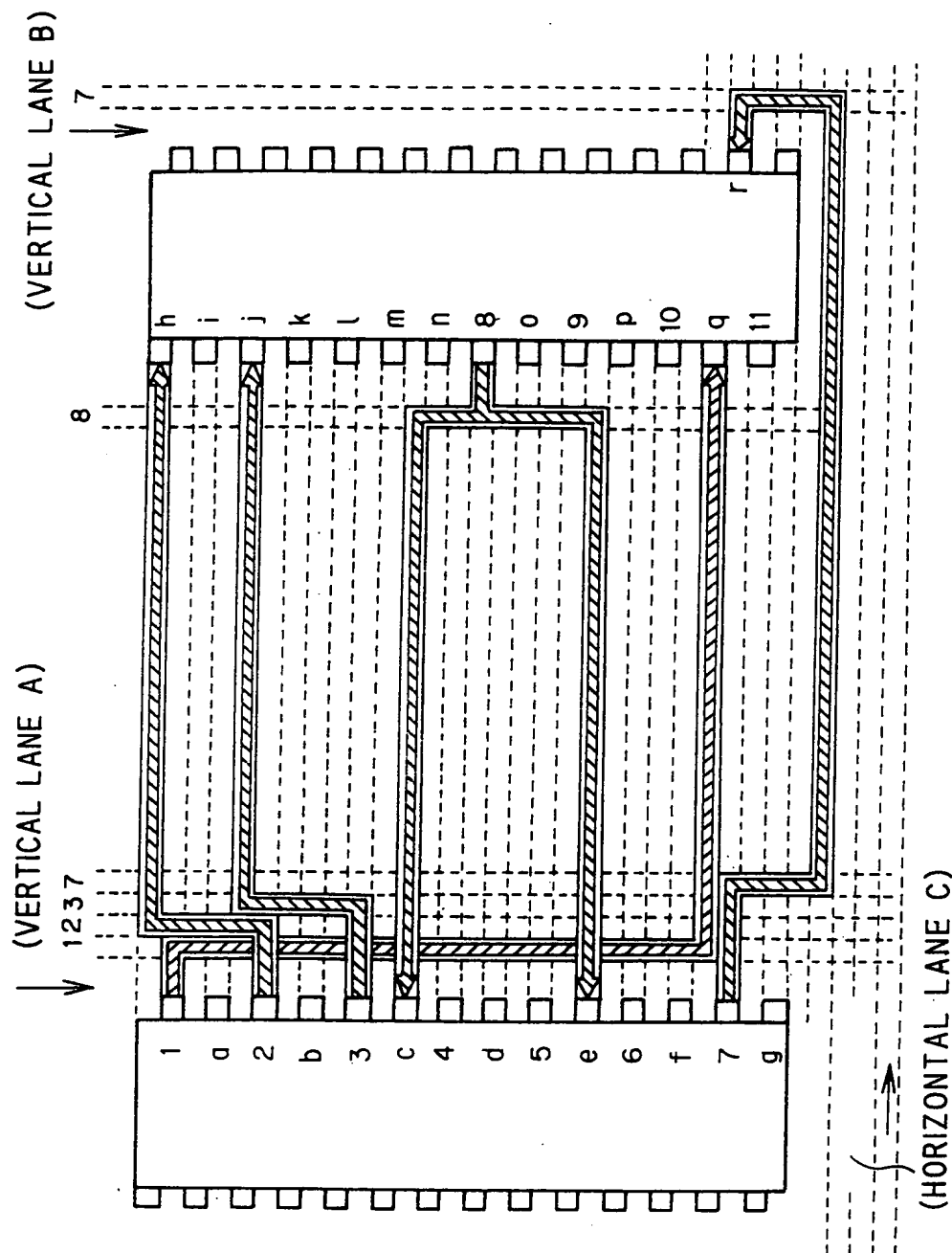


Fig. 54

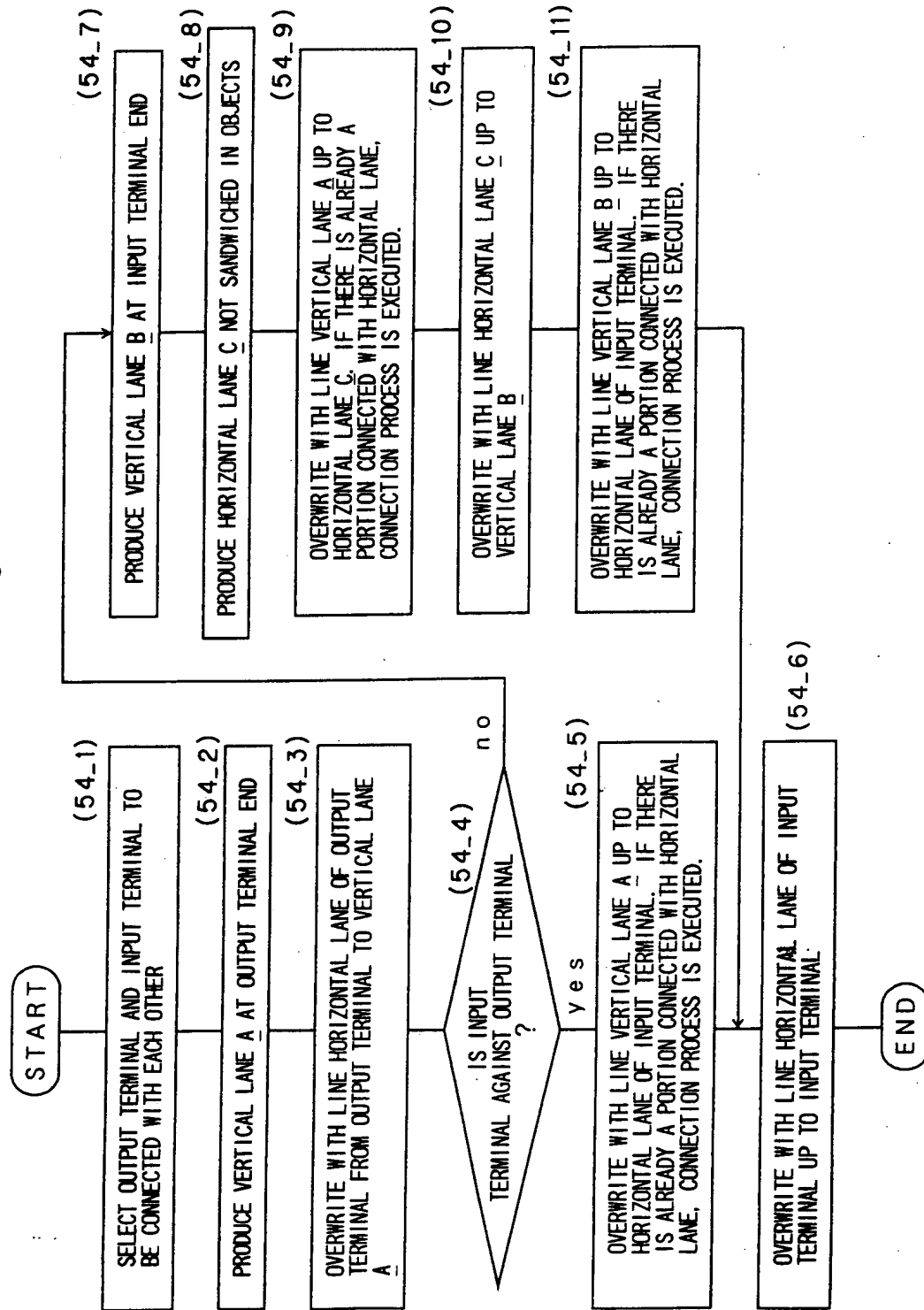


Fig. 55

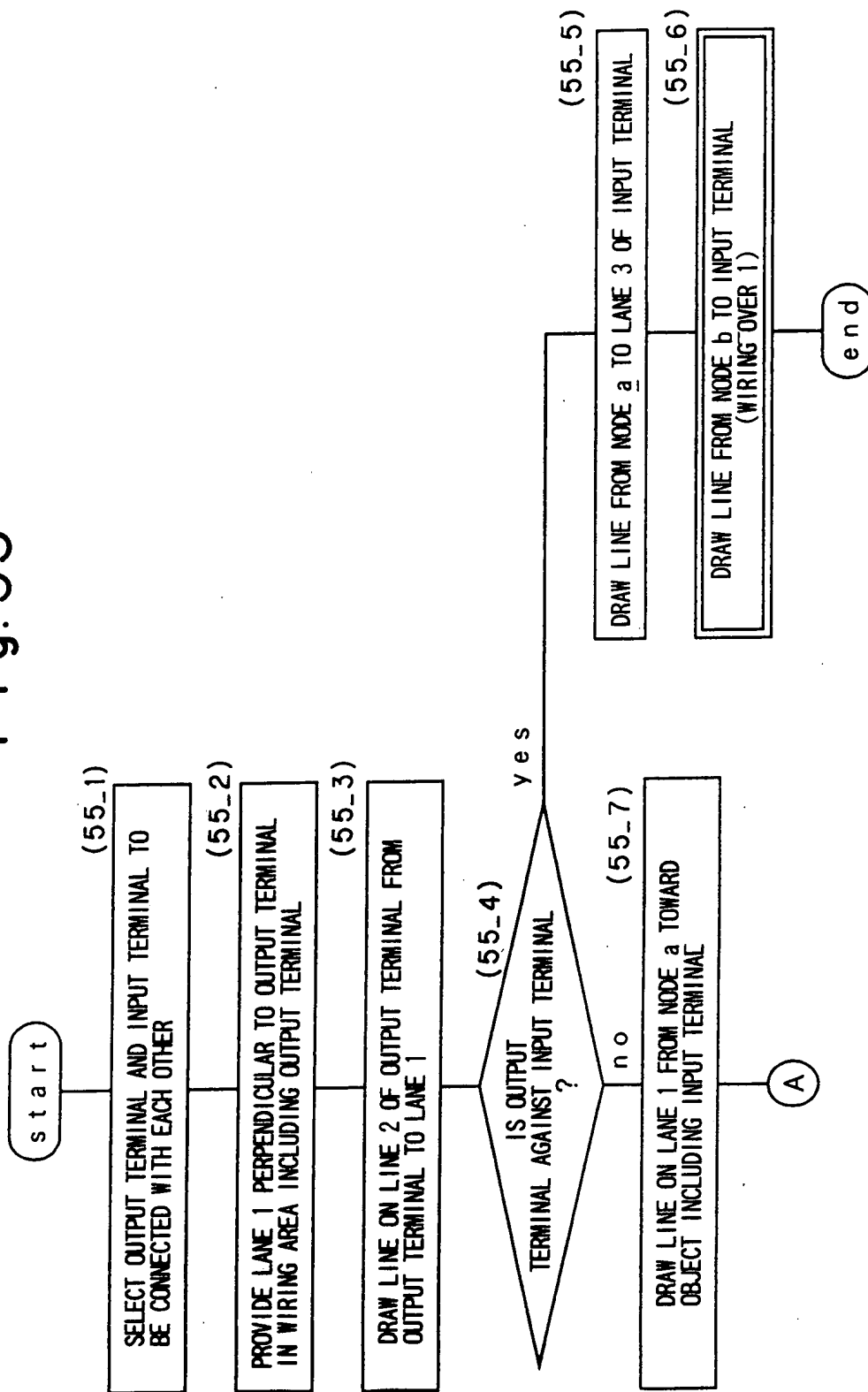


Fig. 56

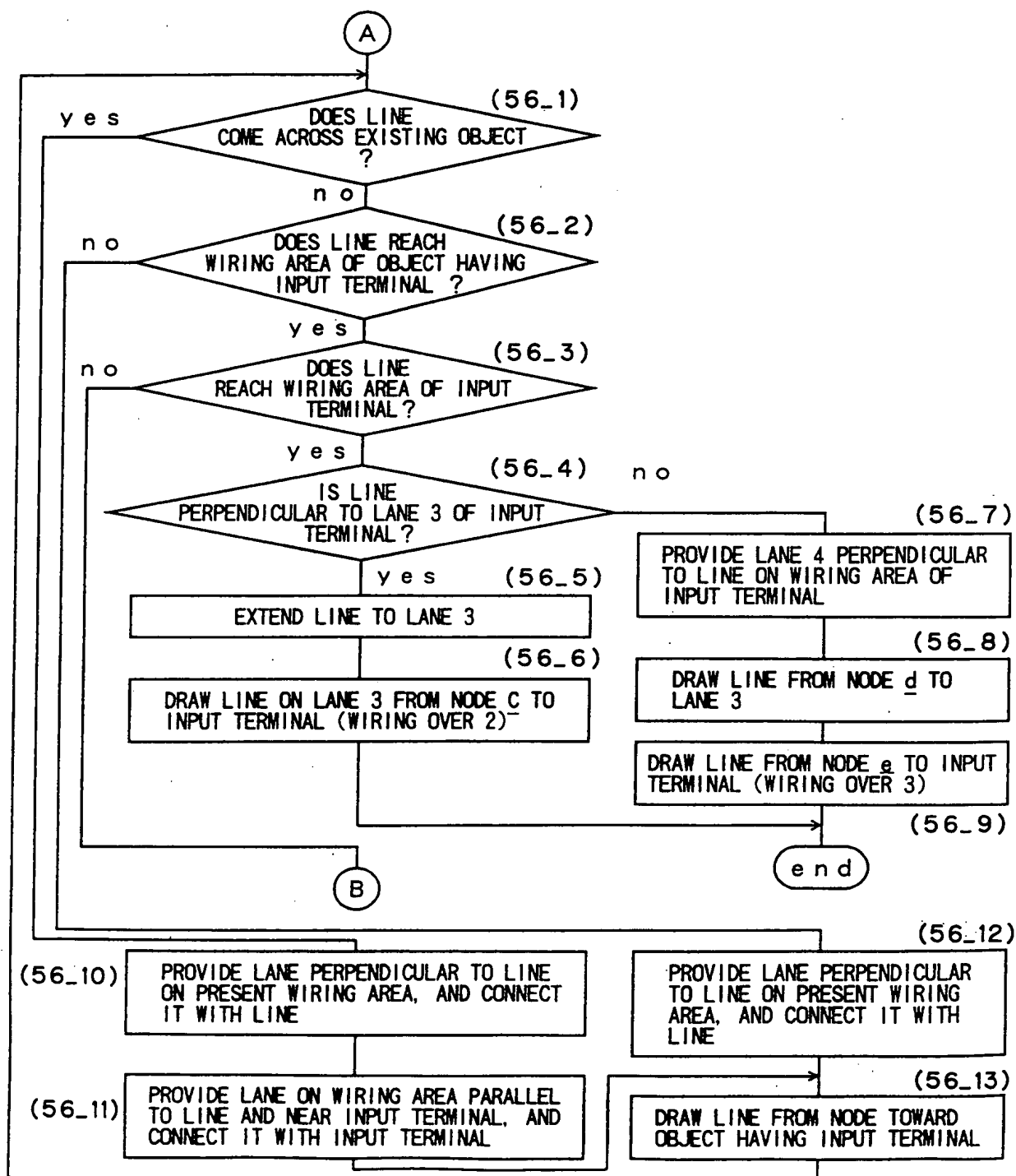


Fig. 57

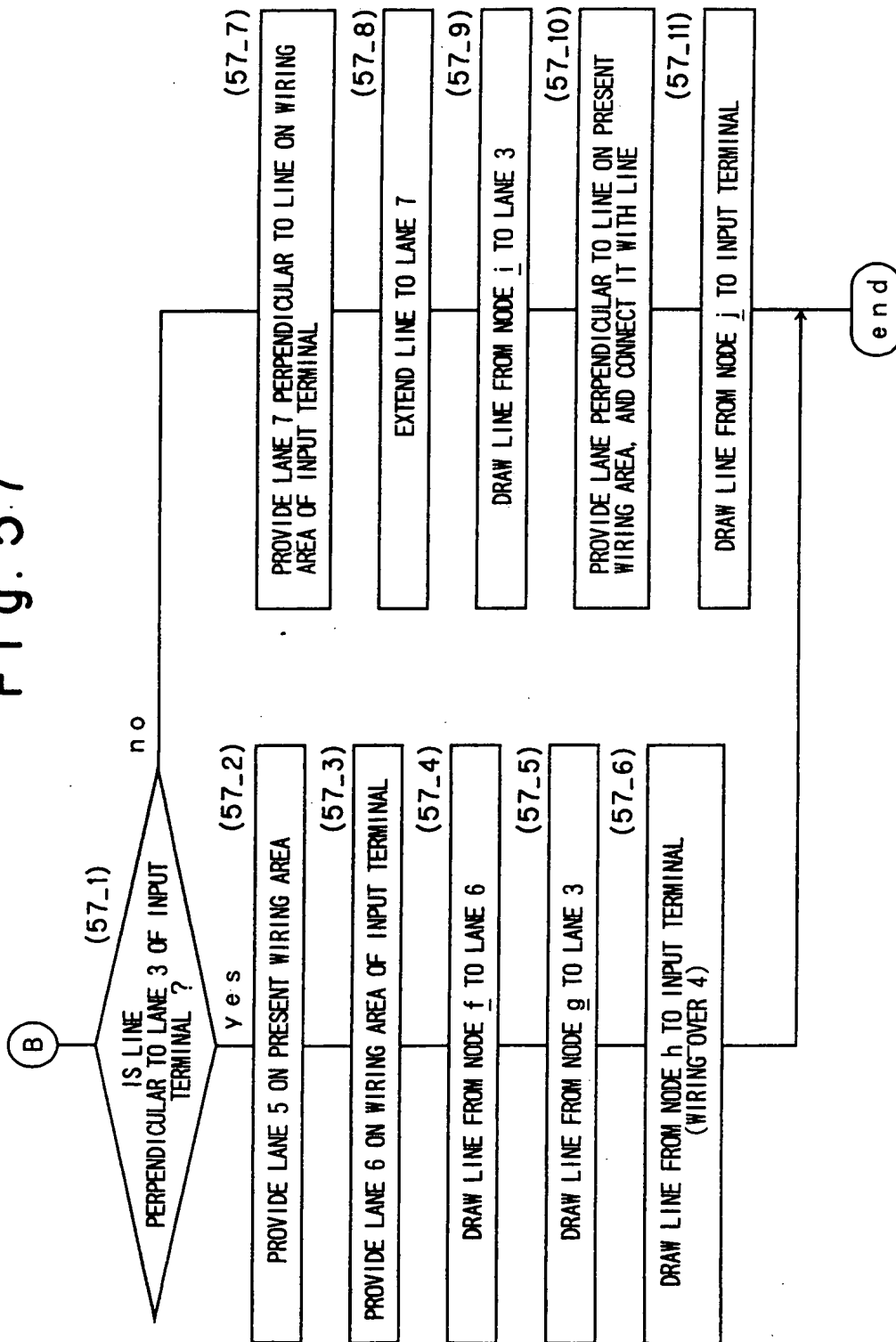


Fig. 58

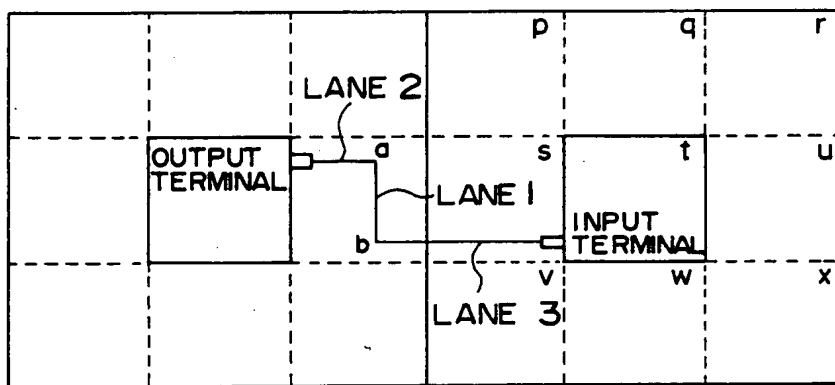
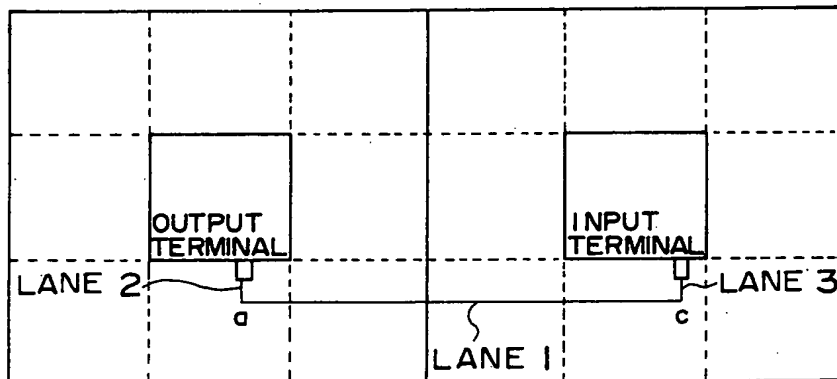
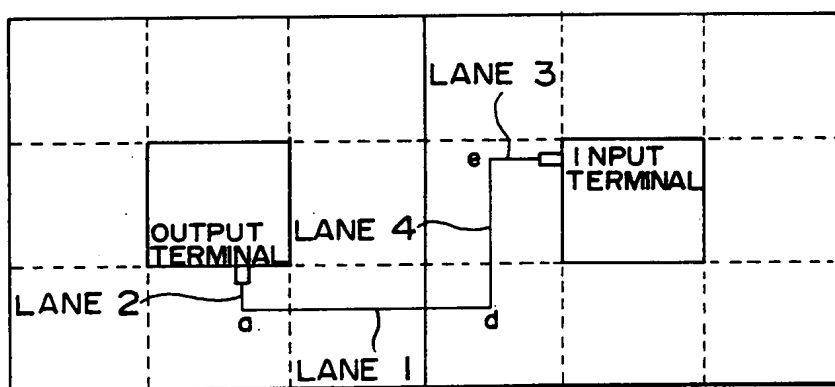


Fig. 59



F i g. 60



F i g. 61

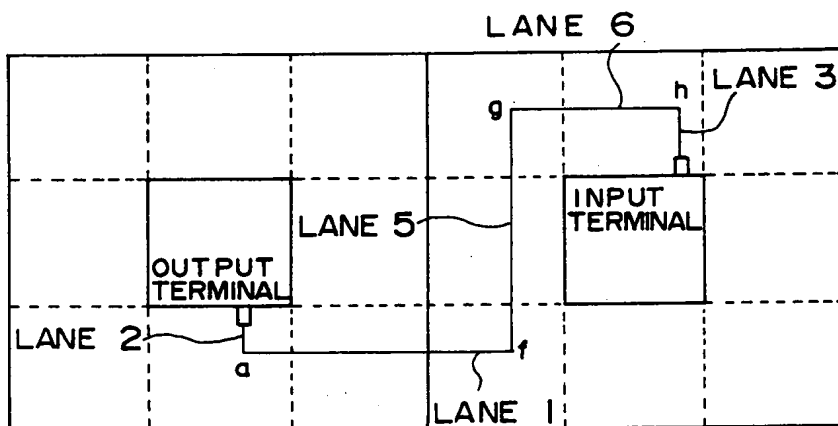


Fig. 62

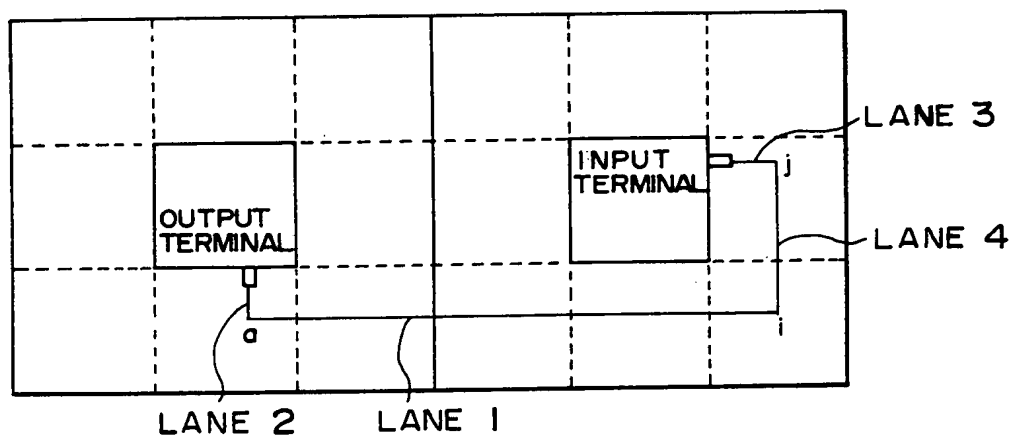


Fig. 63(A)

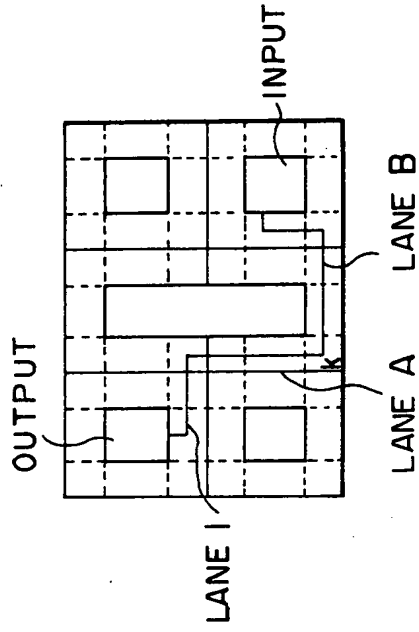


Fig. 63(C)

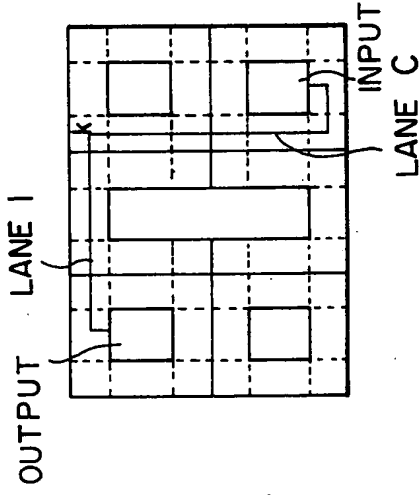


Fig. 63(B)

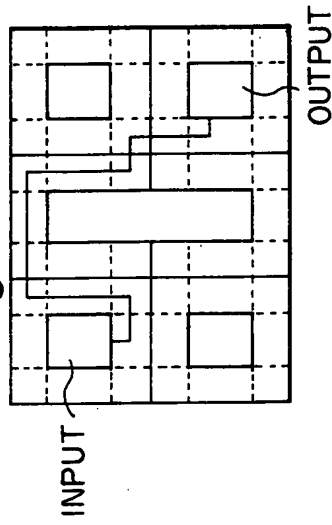
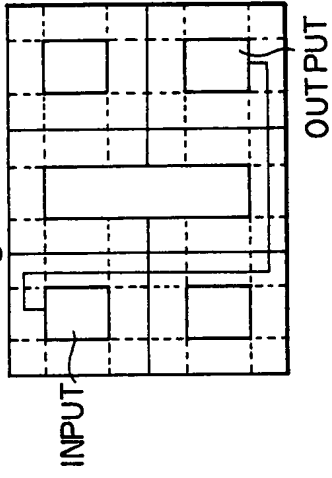


Fig. 63(D)



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Fig. 64

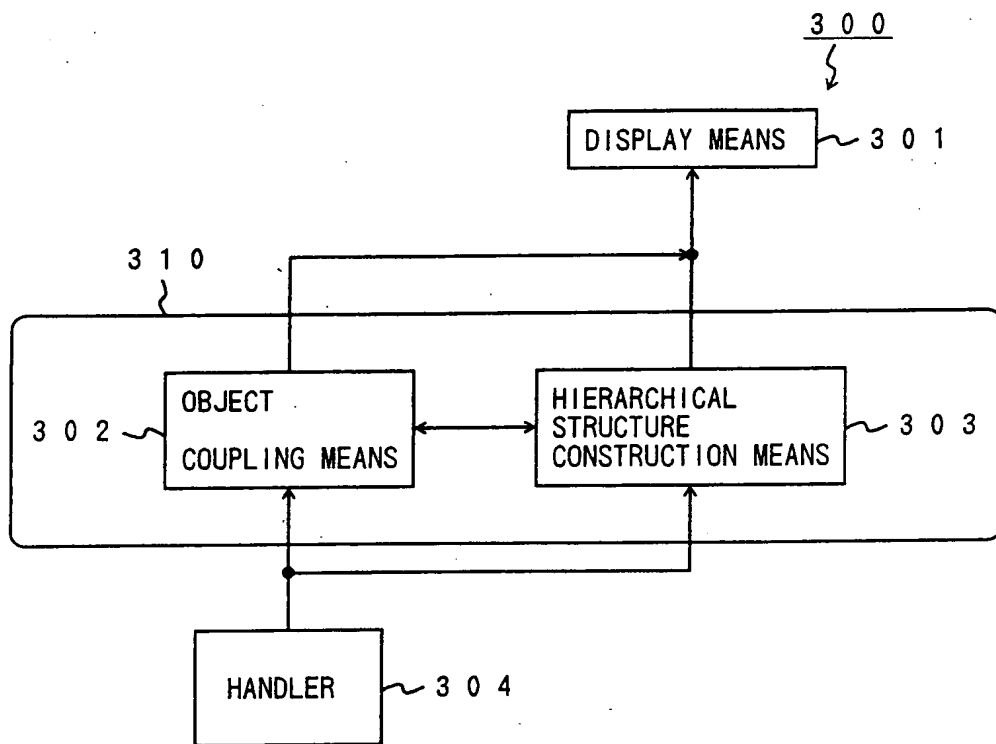


Fig. 65

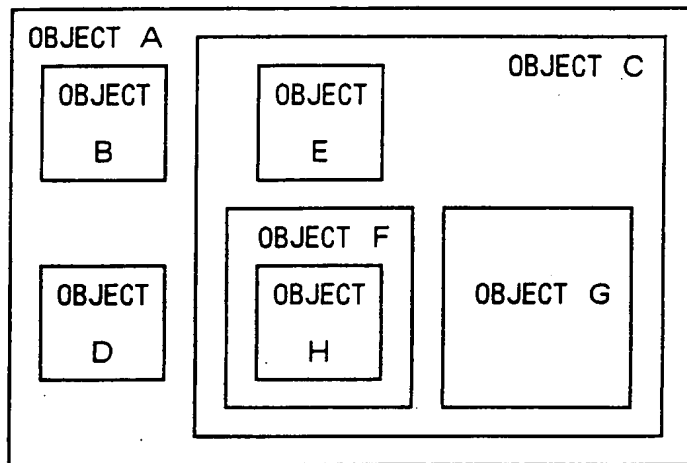


Fig. 66

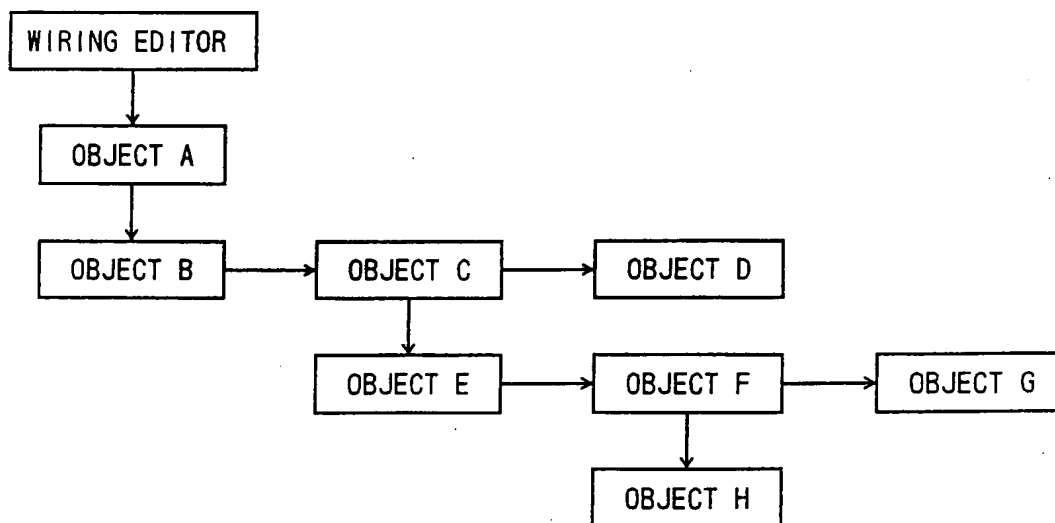


Fig. 67

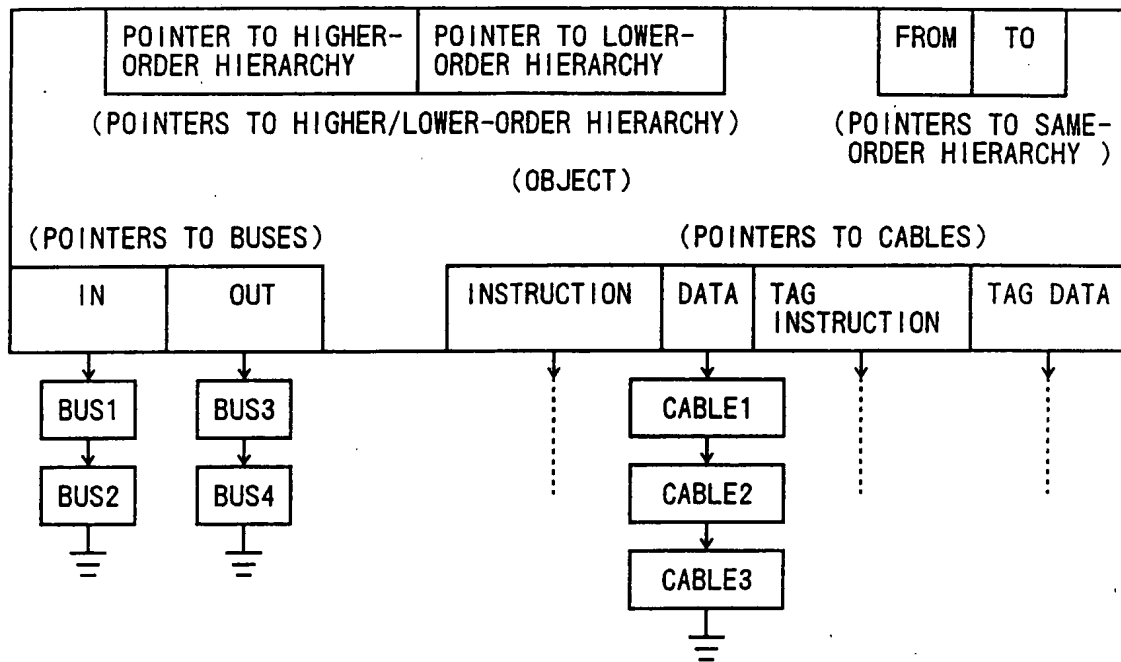


Fig. 68

(BUS)

POINTER TO SUBSTANTIAL OBJECT
POINTER TO BUS OF SUBSTANTIAL OBJECT
POINTER TO NEXT BUS
OTHER DATA

Fig. 69

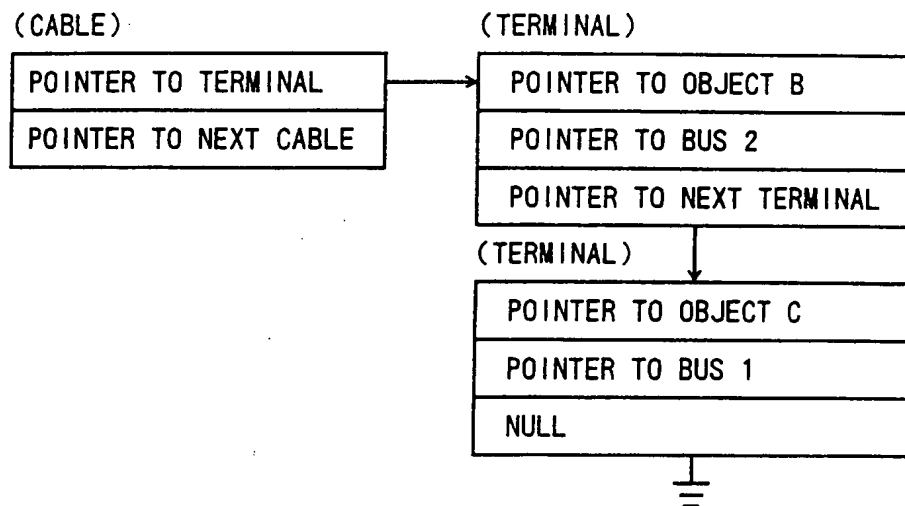


Fig. 70

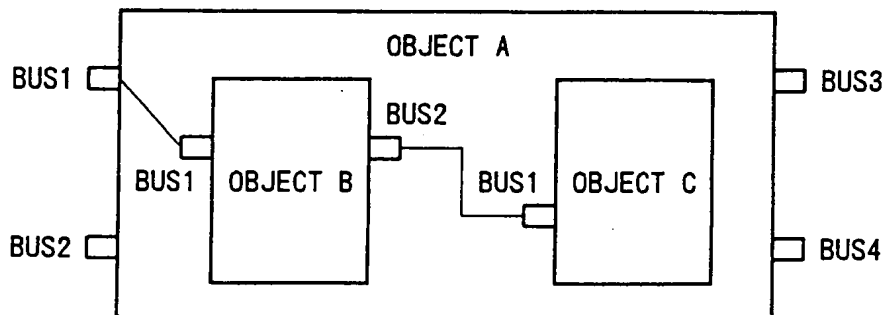


Fig. 71

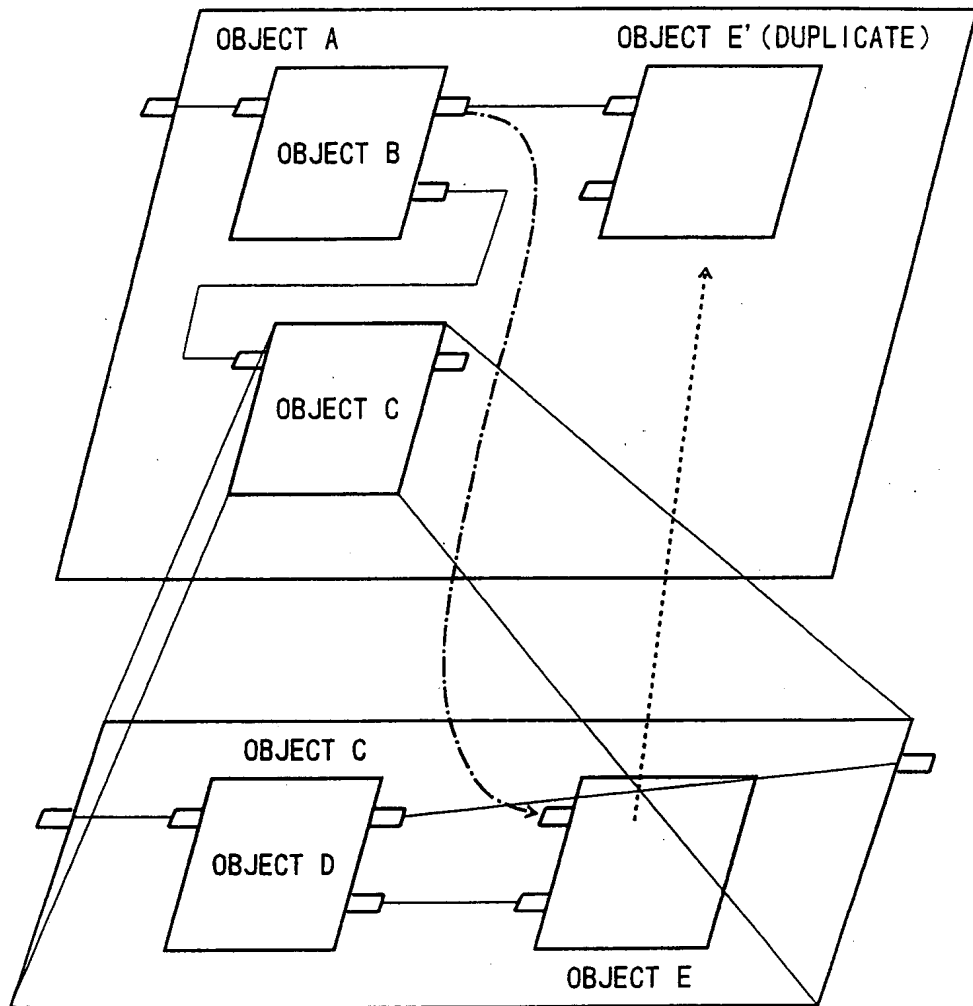


Fig. 72

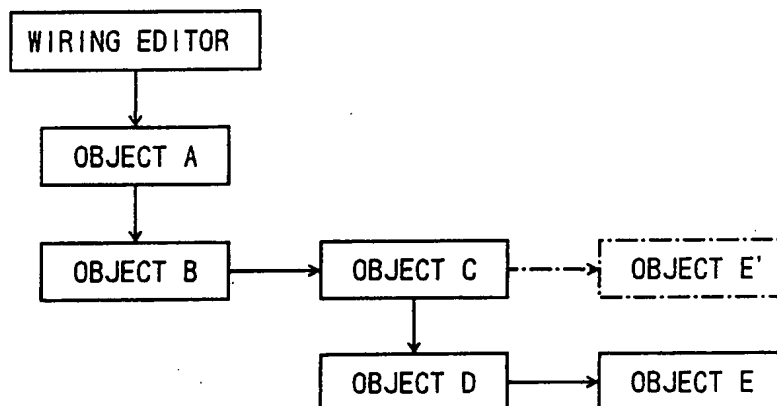


Fig. 73

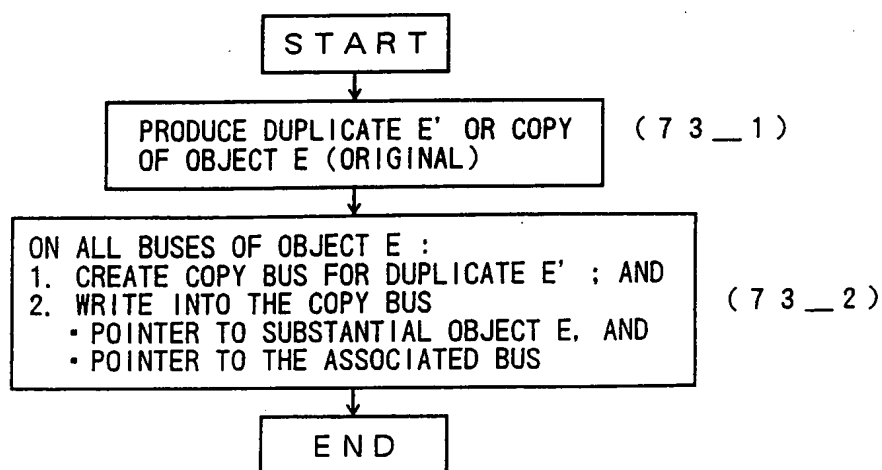


Fig. 74

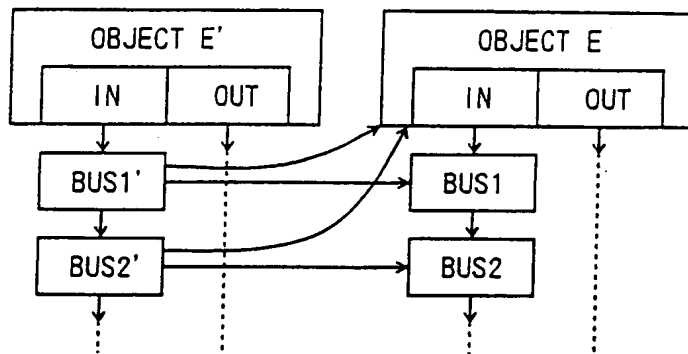


Fig. 75

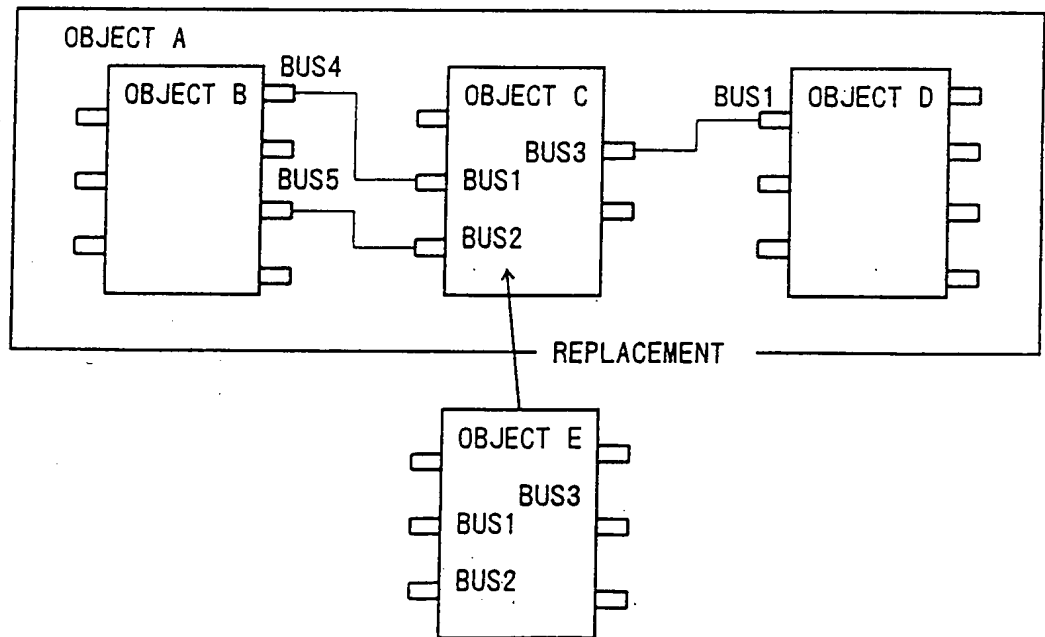


Fig. 76

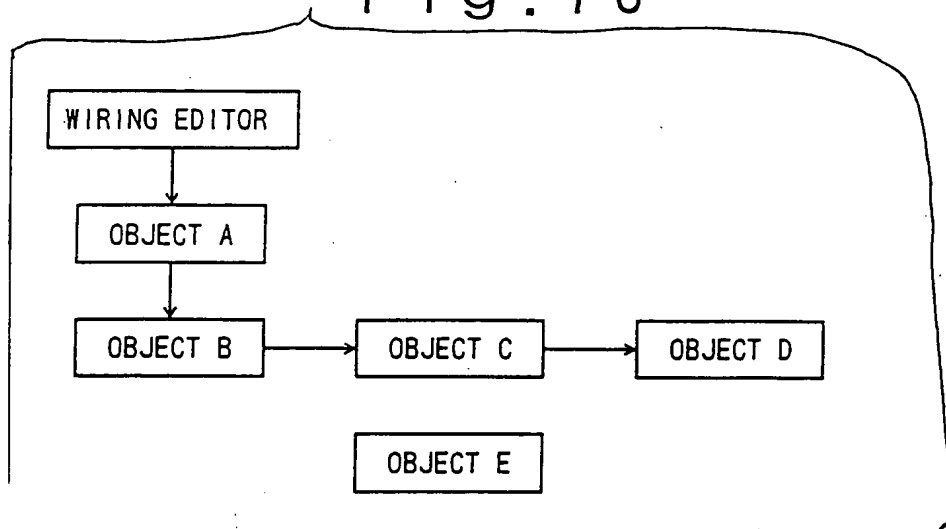


Fig. 77

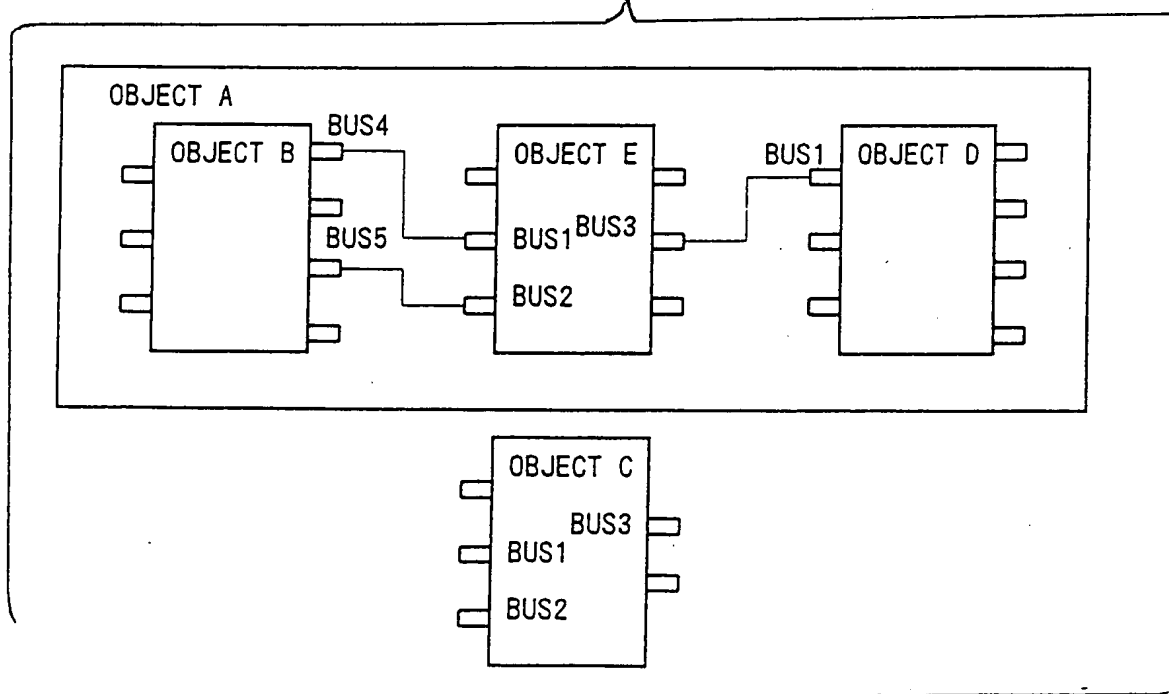


Fig. 78

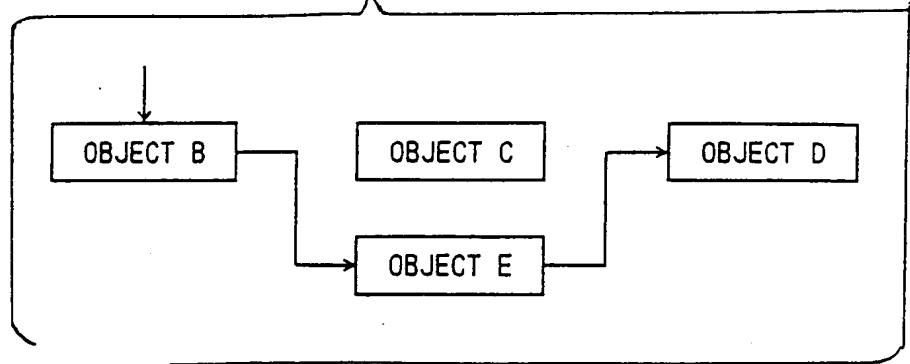


Fig. 79

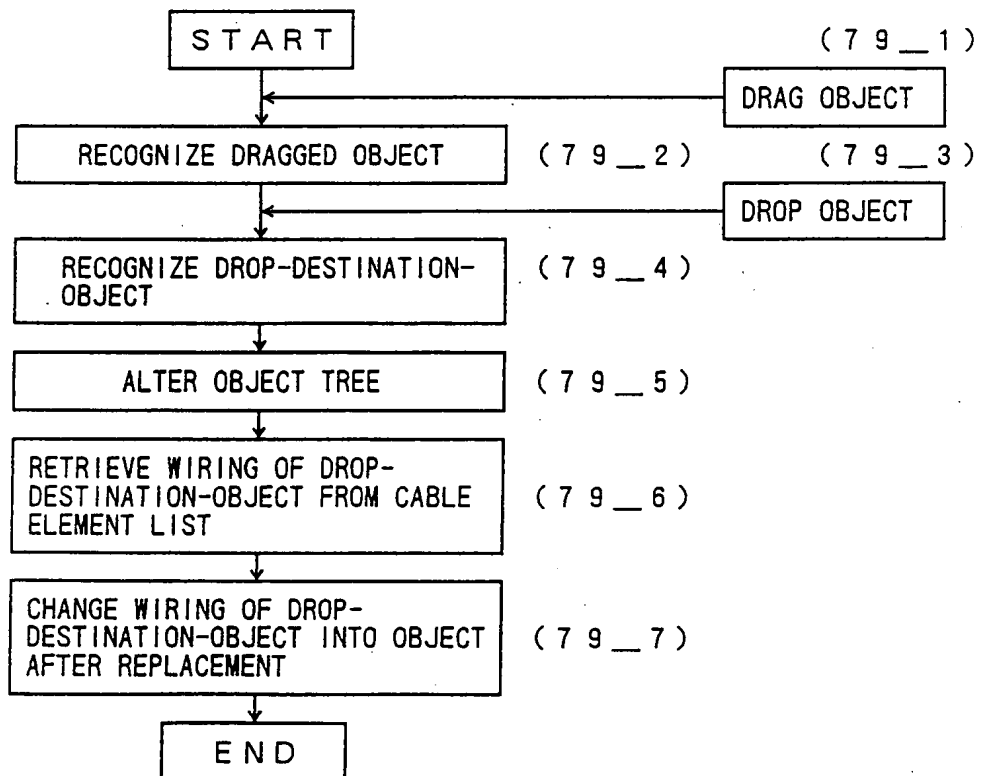
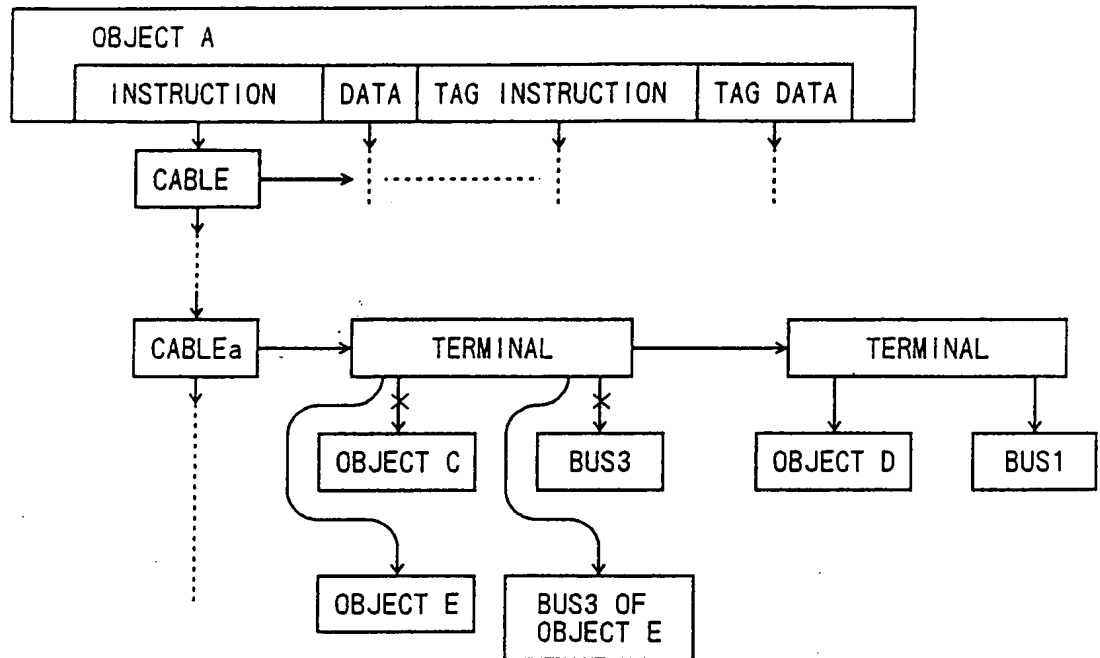
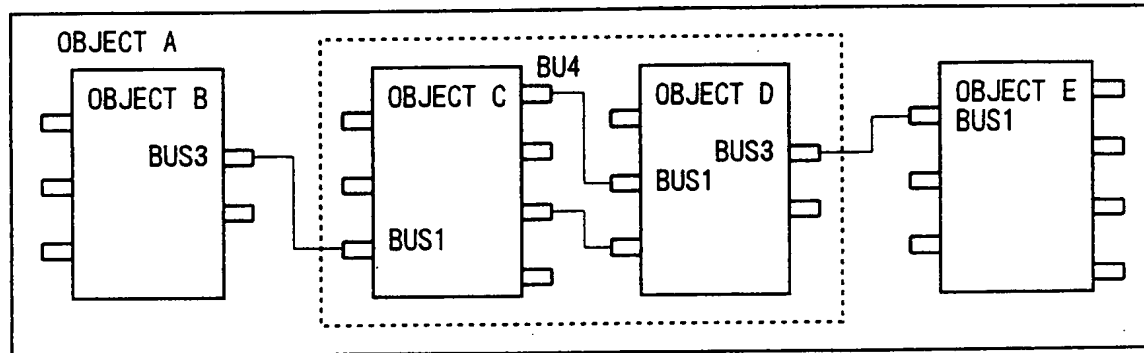


Fig. 80



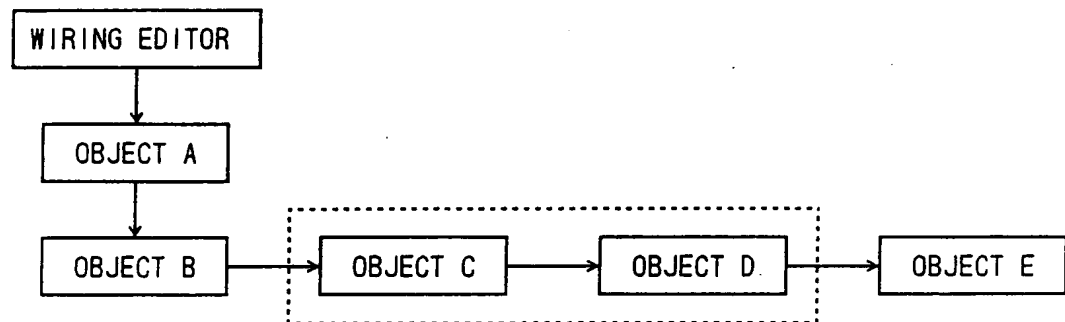
09765430-050701
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Fig. 81



095430-00000000

Fig. 82



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FD4090-08459460

Fig. 83

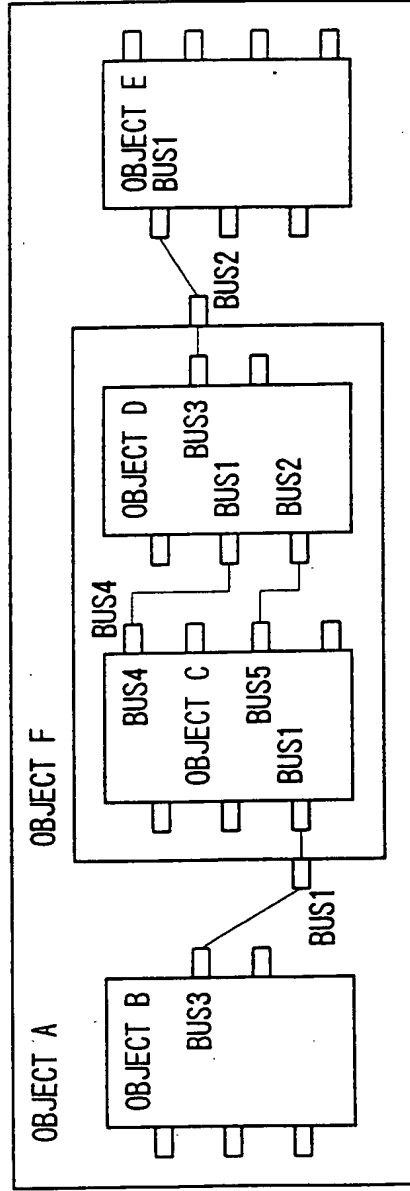


Fig . 84

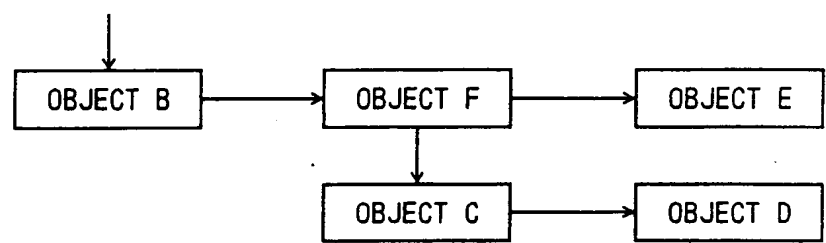
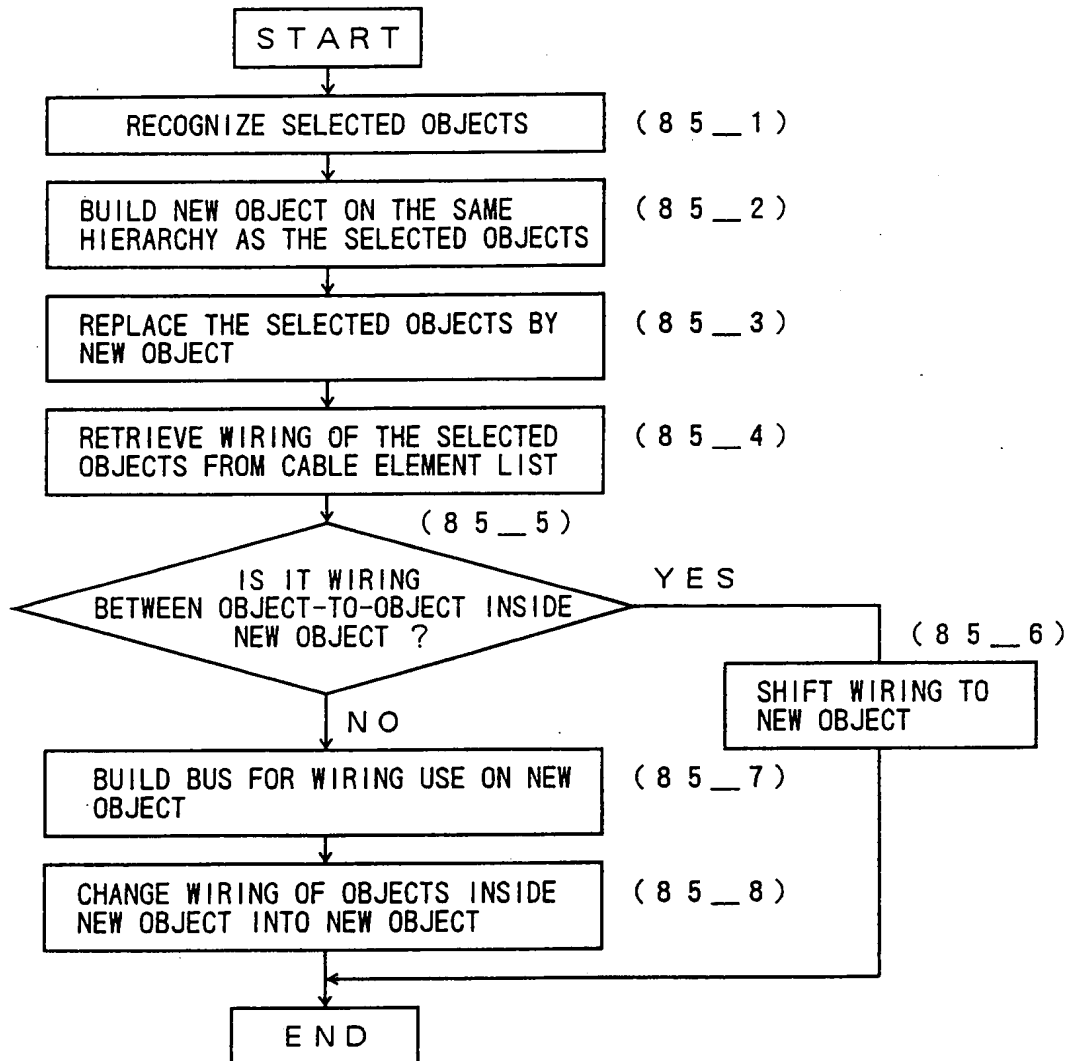


Fig. 85



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Fig. 86

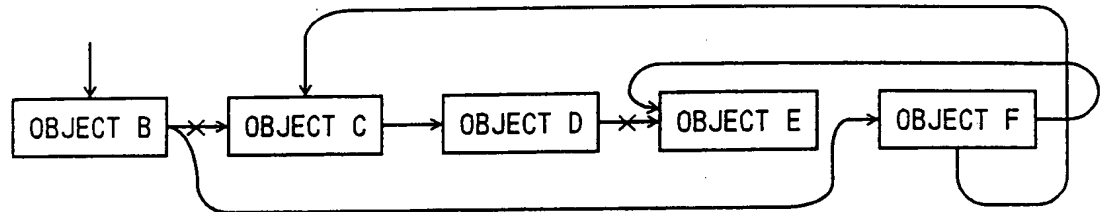


Fig. 87

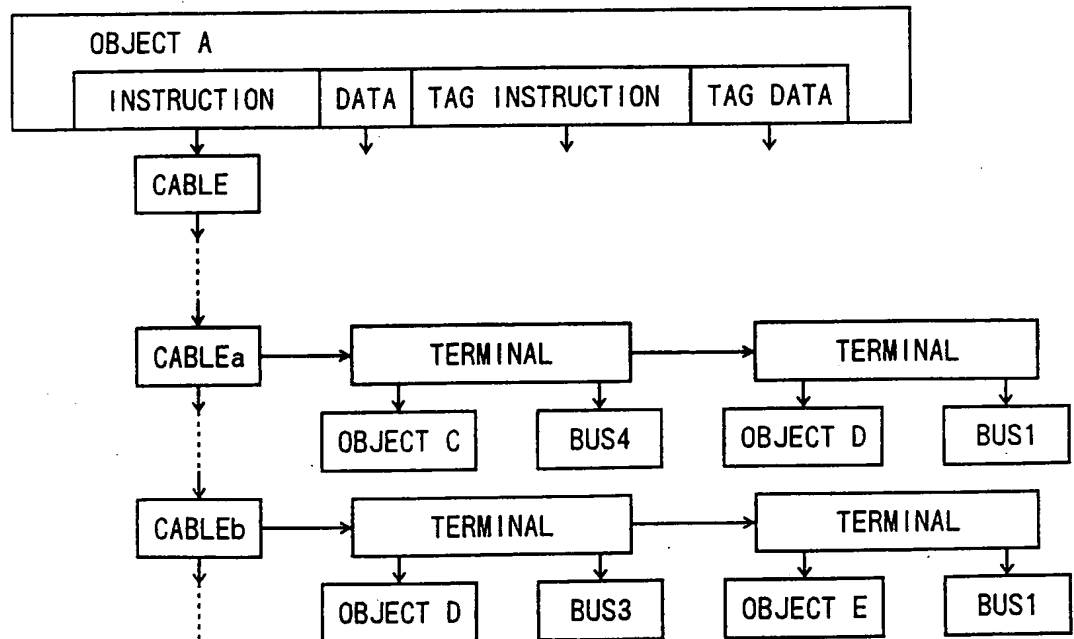


Fig. 88

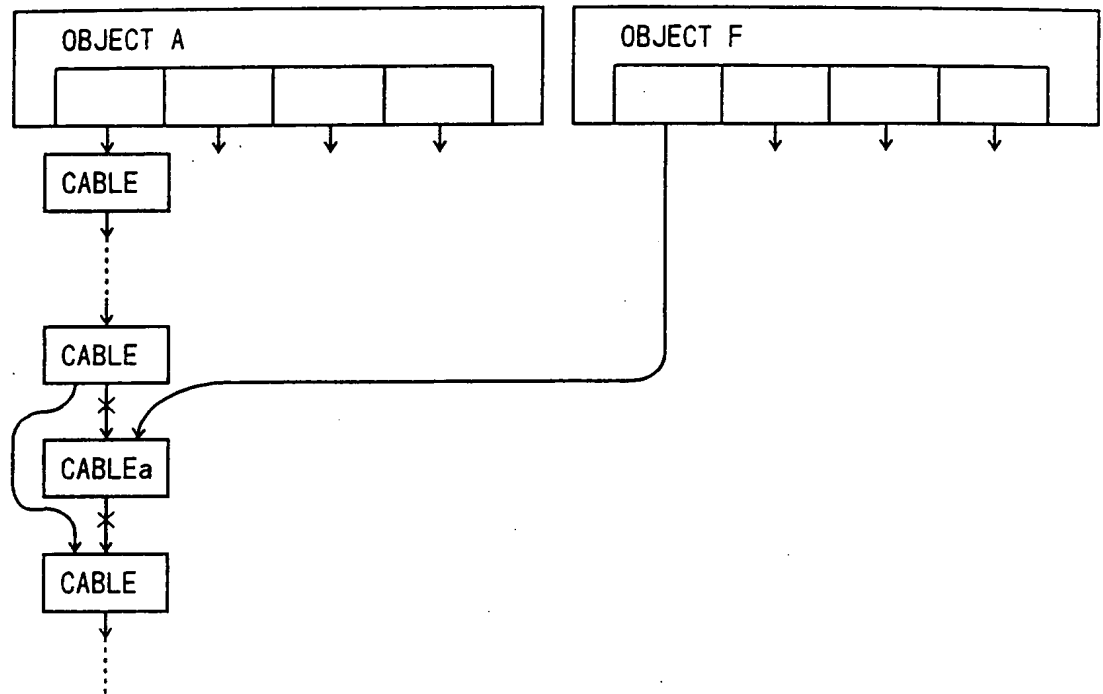
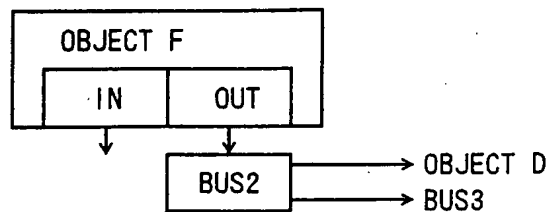


Fig. 89

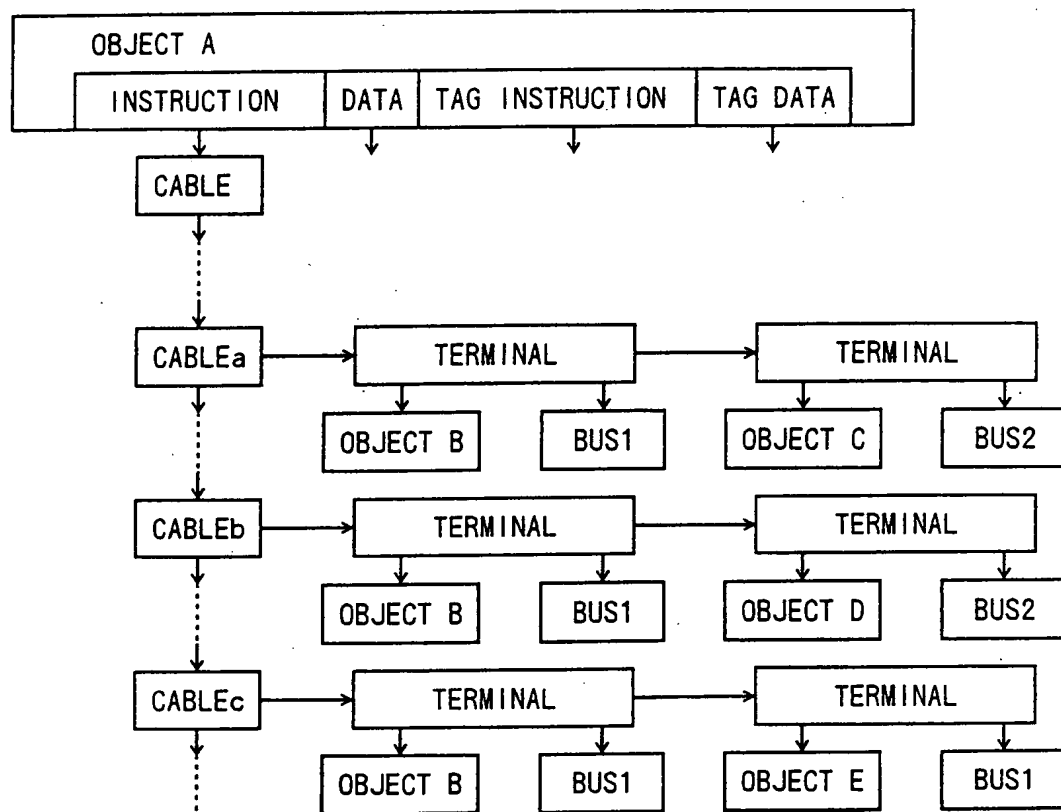


```

graph LR
    In(( )) --> CABLEb[CABLEb]
    CABLEb --> Dots[...]
    CABLEb --> T1[TERMINAL]
    T1 -- X --> OD[OBJECT D]
    T1 -- X --> B3[BUS3]
    T1 --> OF[OBJECT F]
    B3 --> B2[BUS2]
    T1 --> T2[TERMINAL]
    T2 --> OE[OBJECT E]
    T2 --> B1[BUS1]
  
```

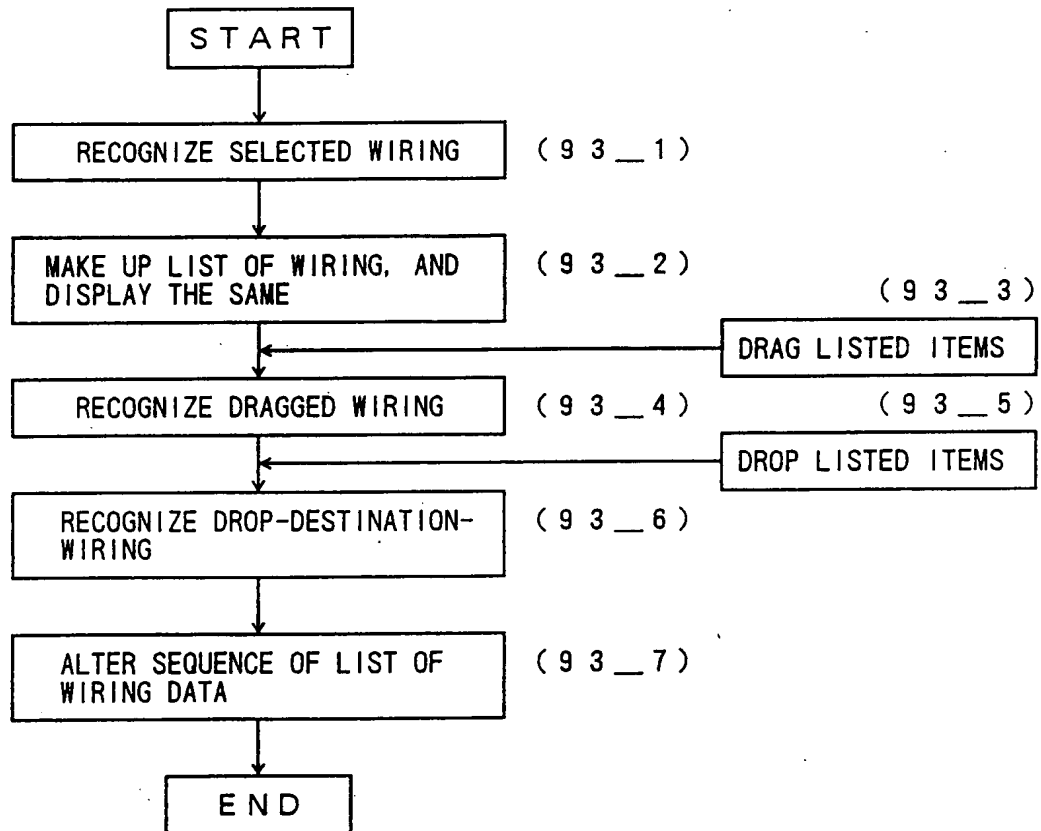
The diagram illustrates a hierarchical bus system. On the left, a box labeled "OBJECT A" contains "BUS1" and "OBJECT B". "BUS1" has four pins on its right side. A line from the top pin of "BUS1" connects to the top pin of "BUS2" in "OBJECT C". A line from the second pin from the top of "BUS1" connects to the top pin of "BUS2" in "OBJECT D". A line from the bottom pin of "BUS1" connects to the top pin of "BUS1" in "OBJECT E". "OBJECT C" and "OBJECT D" each have "BUS2" with four pins on their right sides. "OBJECT E" has "BUS1" with four pins on its right side.

Fig. 92



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Fig. 93



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Fig. 94

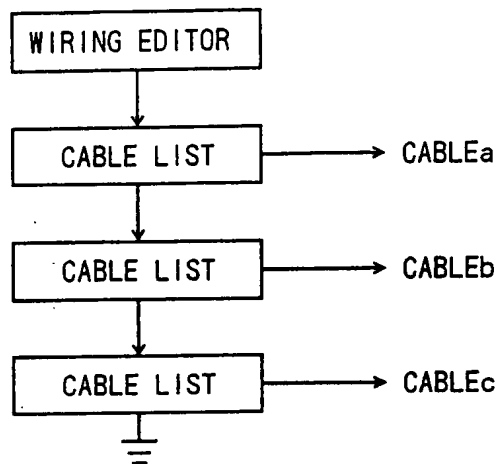
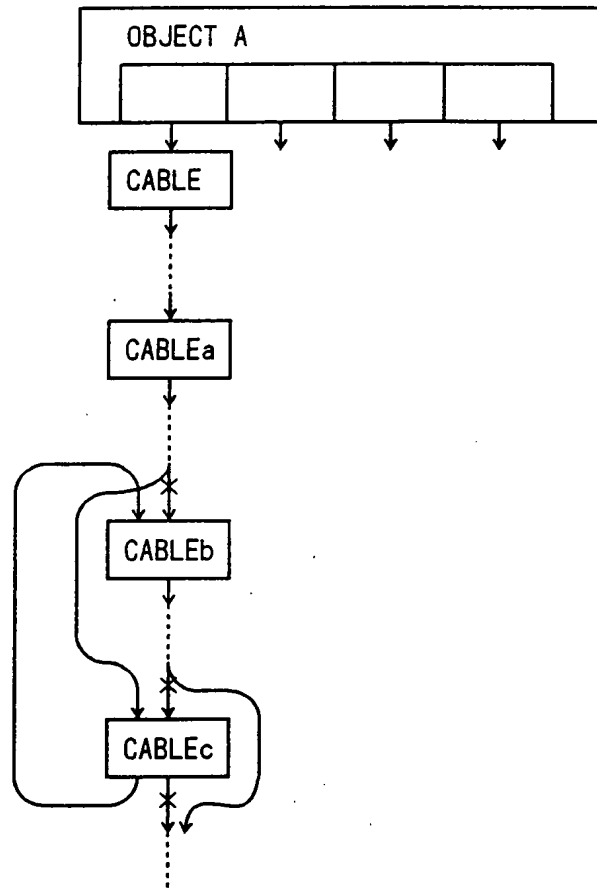


Fig. 95

OBJECT B : BUS1	OBJECT C : BUS2
OBJECT B : BUS1	OBJECT D : BUS2
OBJECT B : BUS1	OBJECT E : BUS1

Fig. 96



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Fig. 97

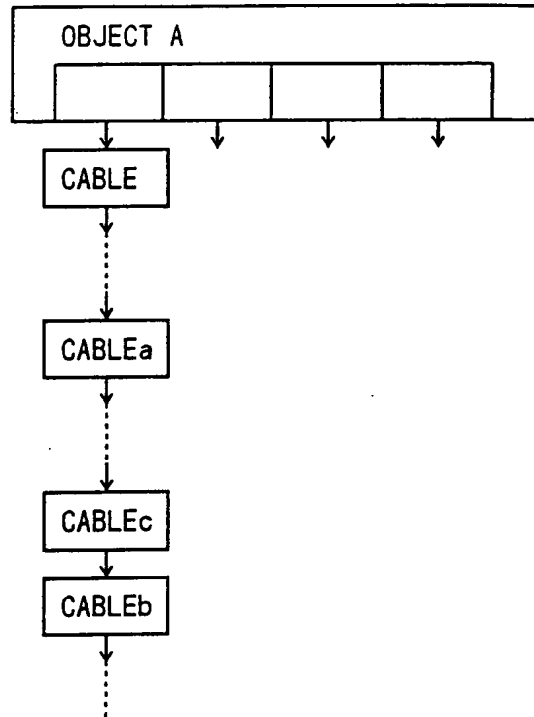
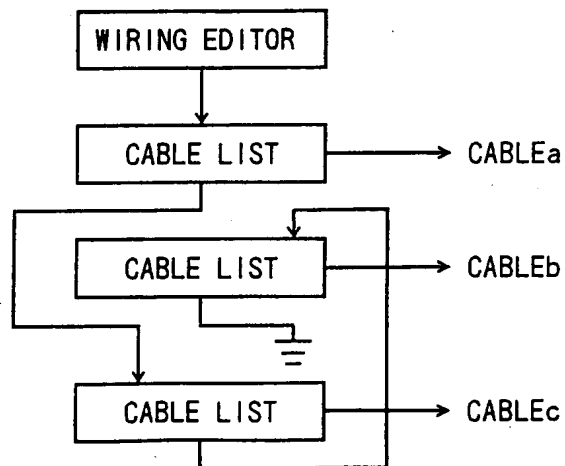


Fig. 98



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FD2050-02459260

Fig. 99

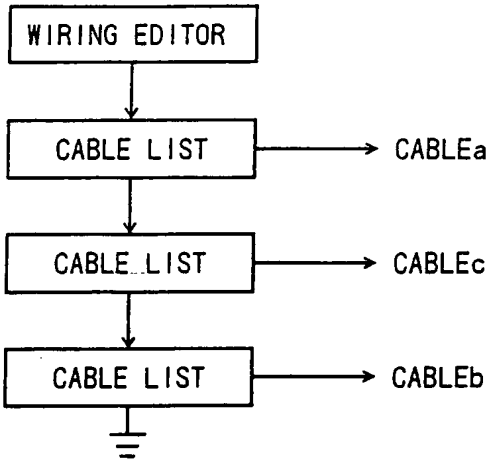


Fig. 100

OBJECT B : BUS1	OBJECT C : BUS2
OBJECT B : BUS1	OBJECT E : BUS1
OBJECT B : BUS1	OBJECT D : BUS2

Fig. 101

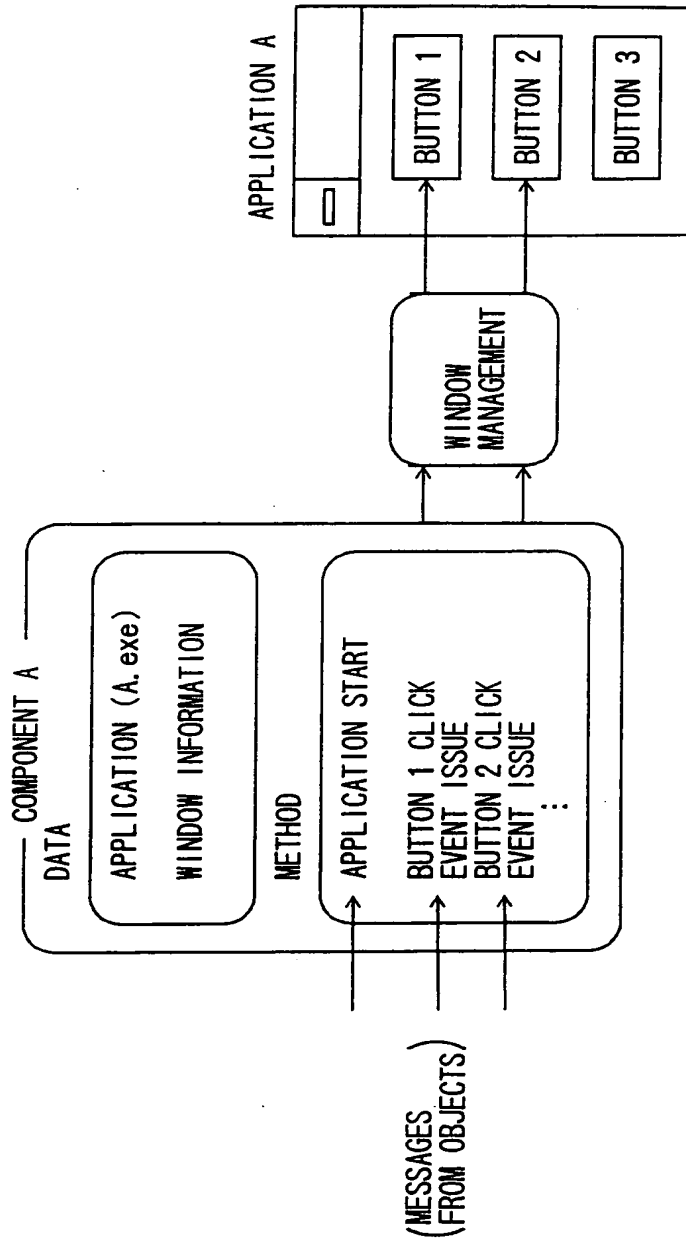


Fig. 102

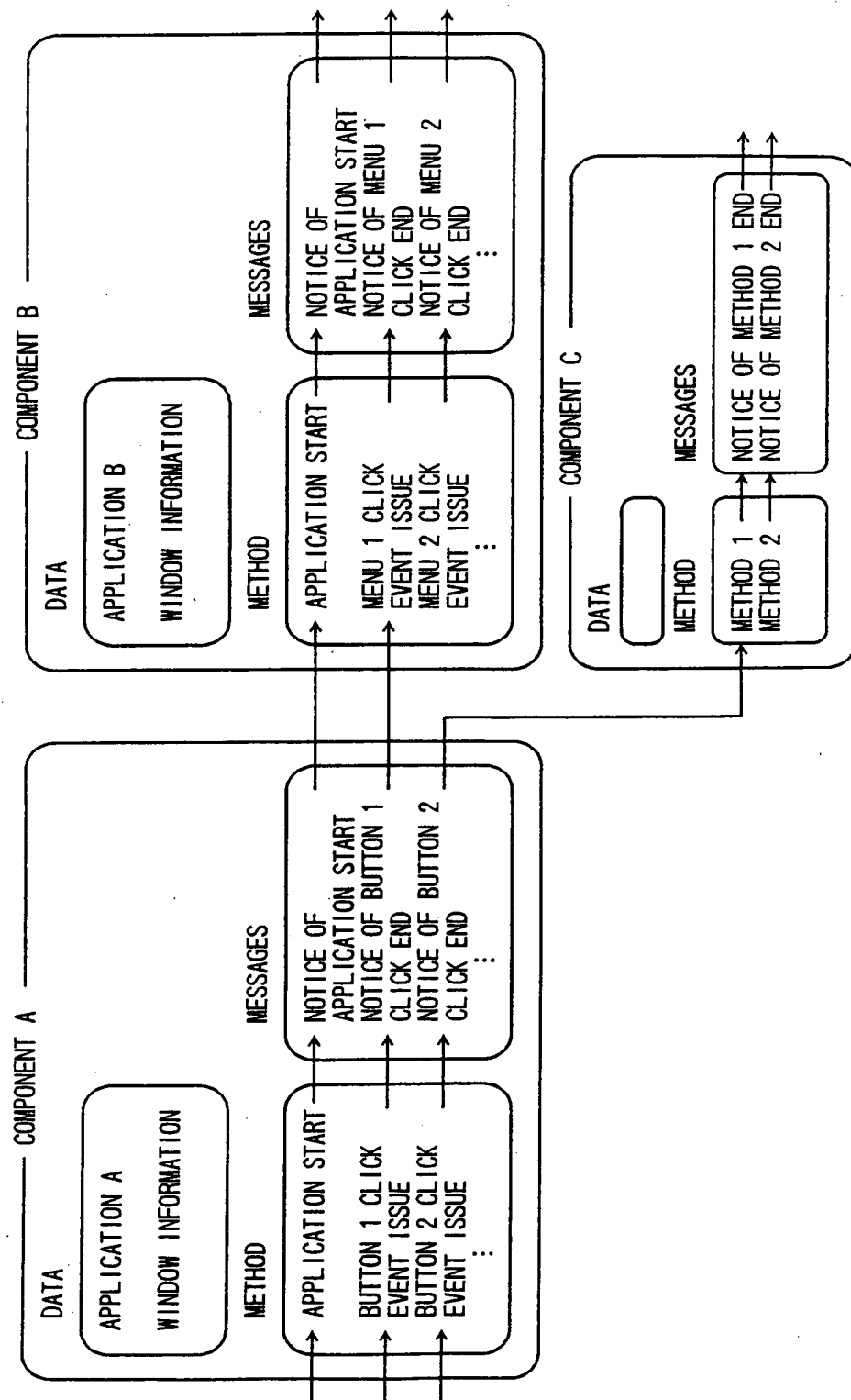
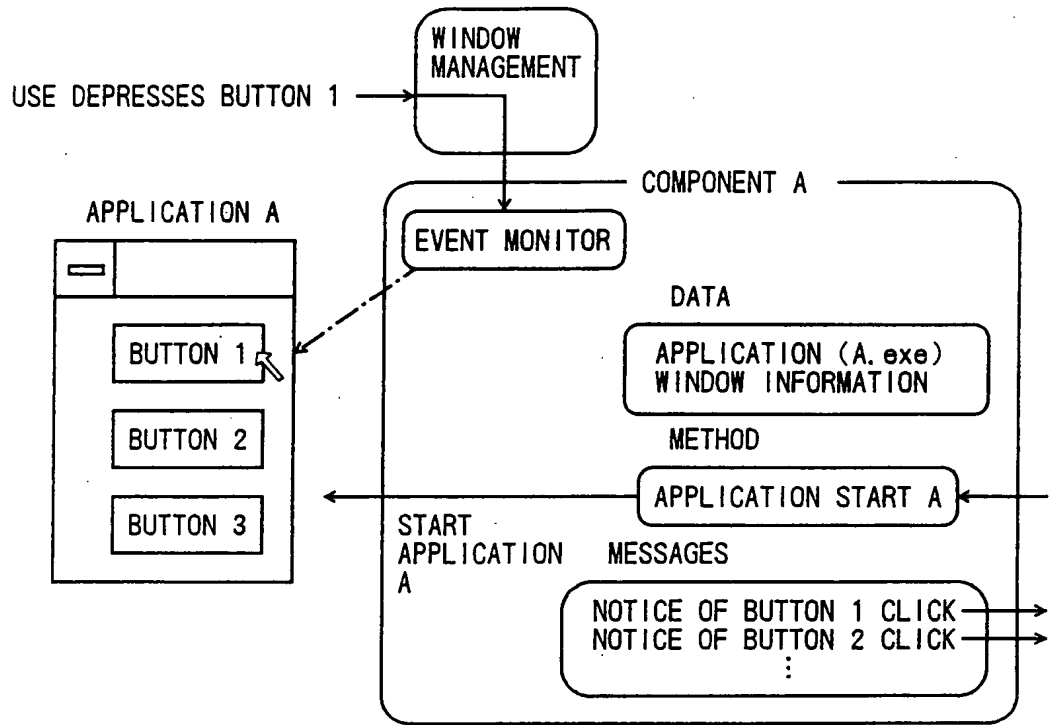


Fig. 103



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Fig. 104

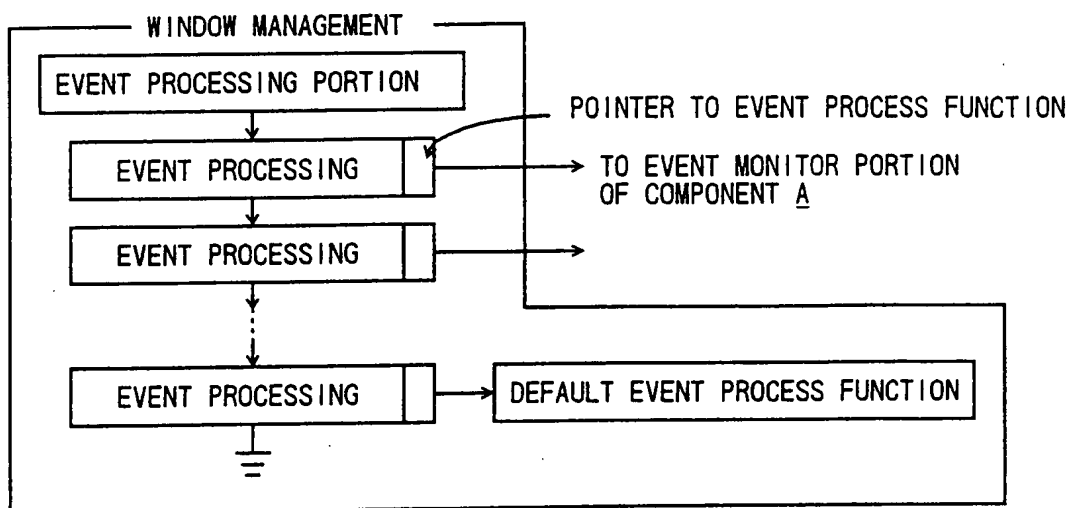


Fig. 105

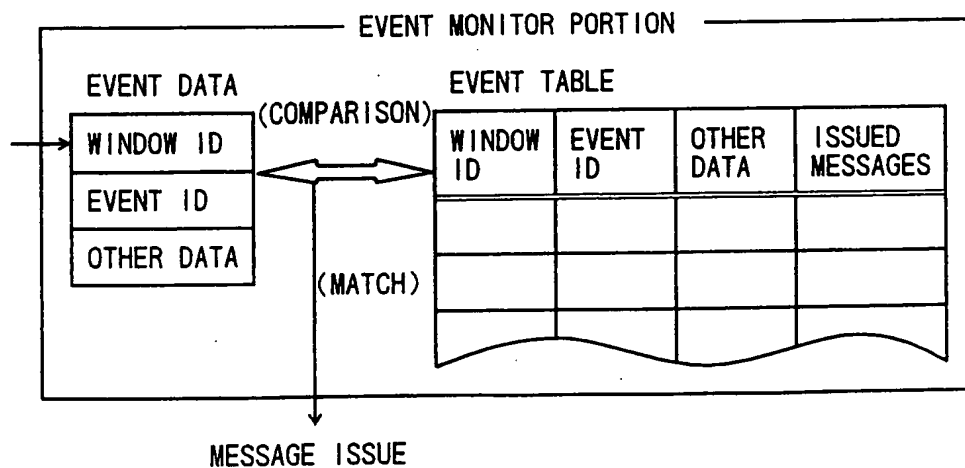


Fig. 106

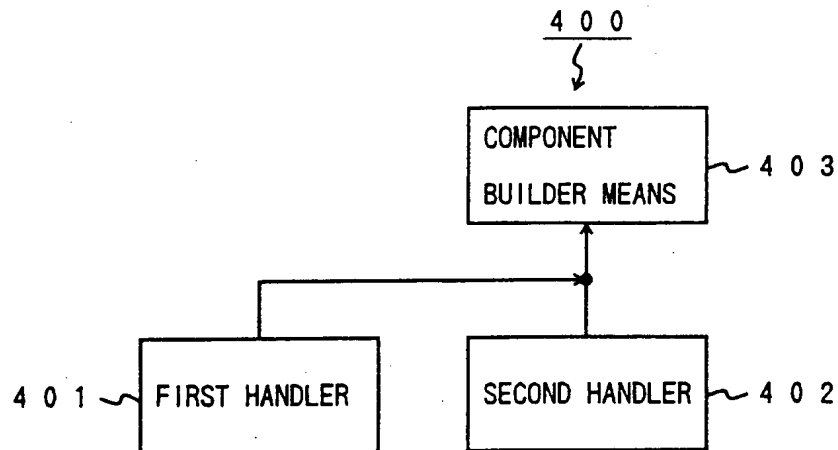
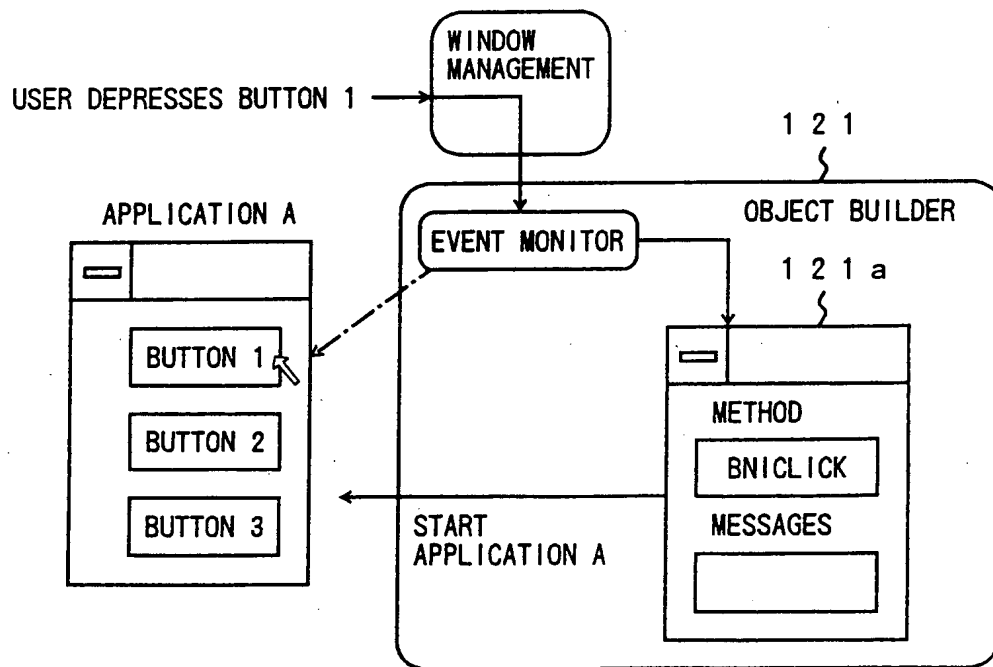
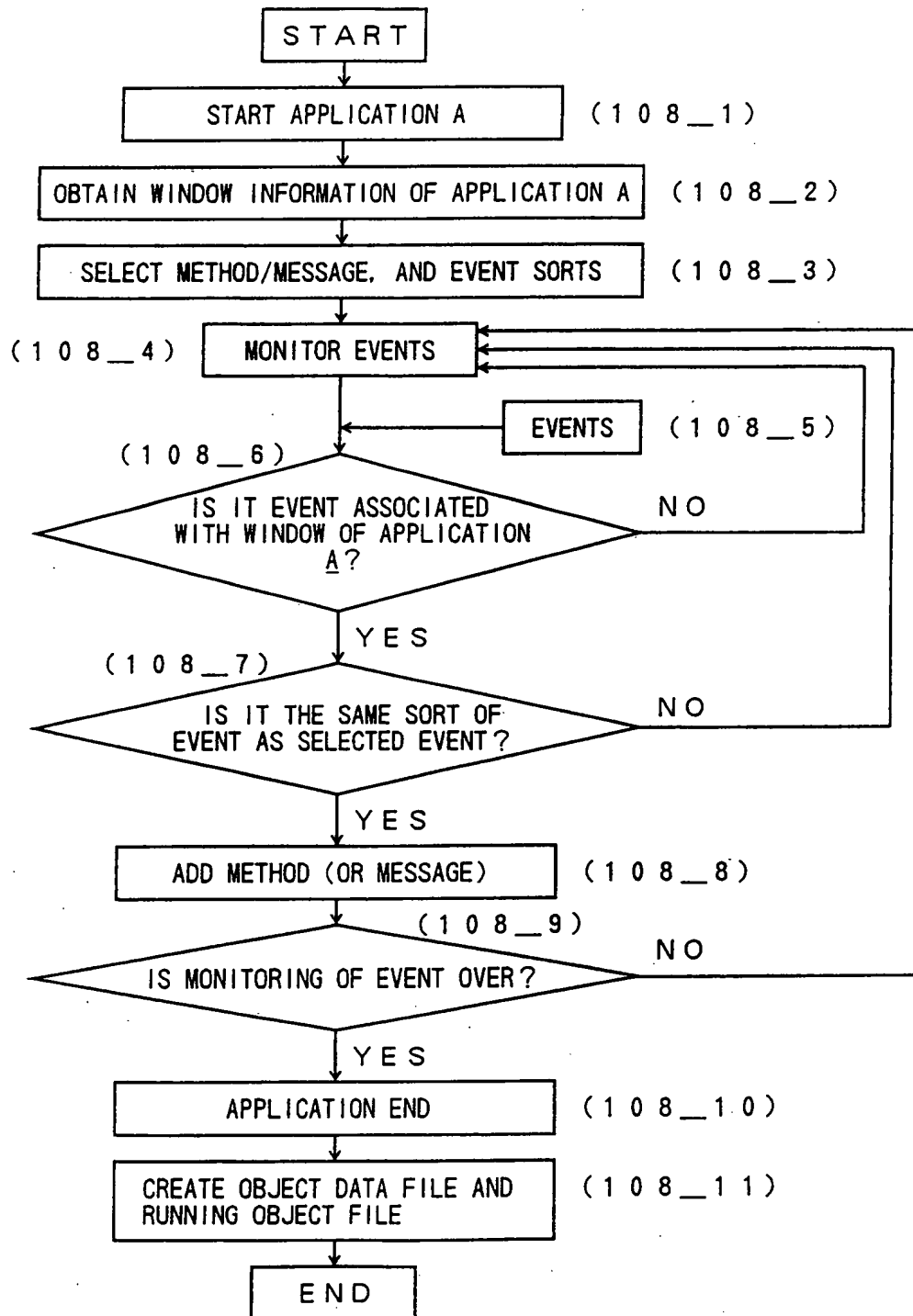


Fig. 107



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Fig.108



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Fig. 109

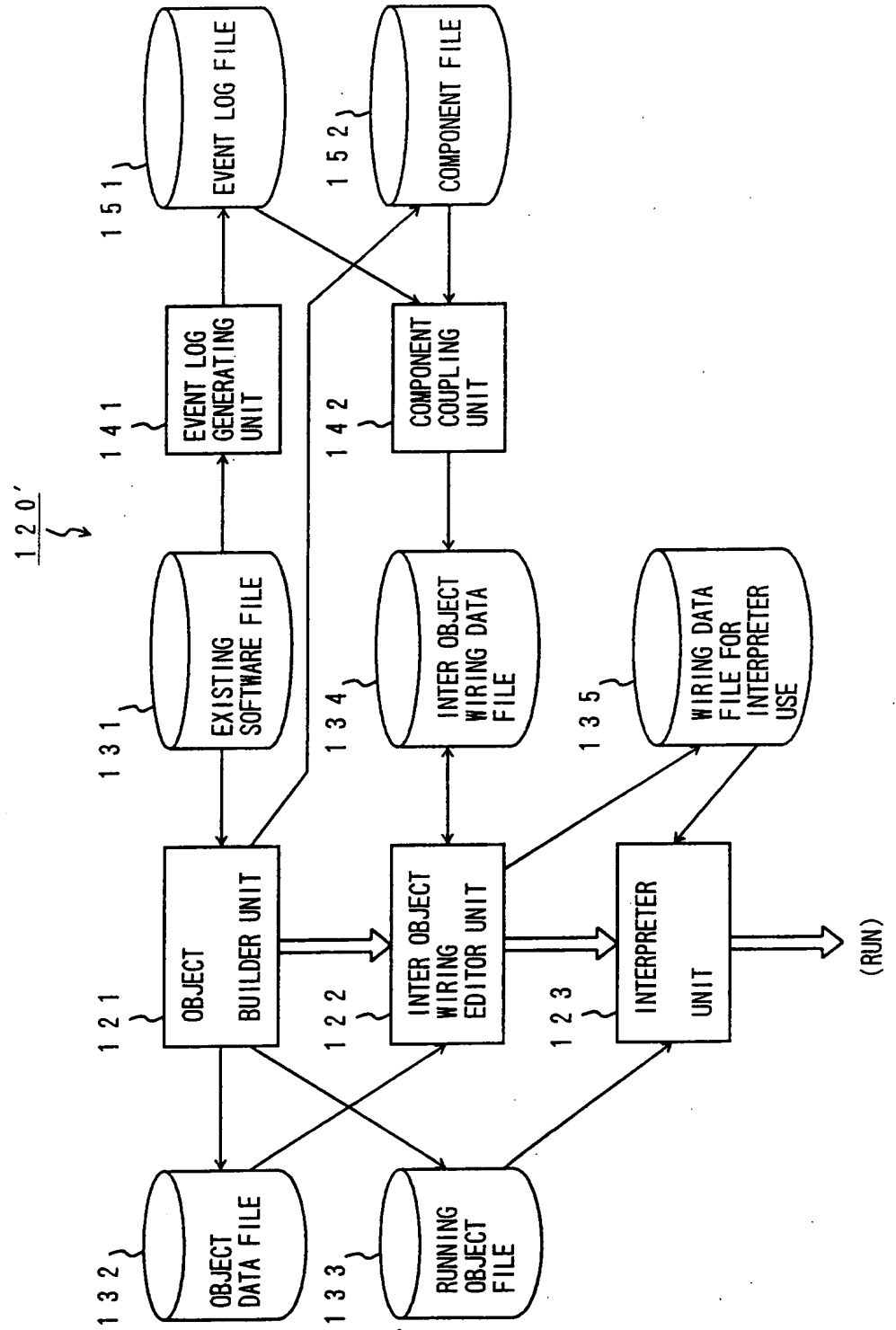
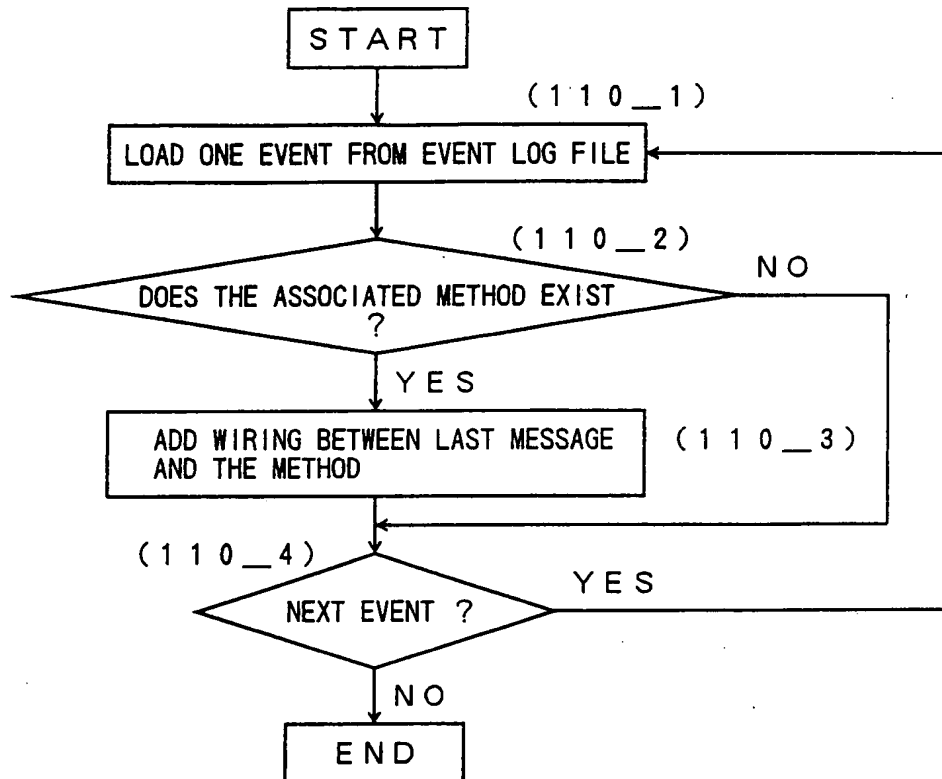
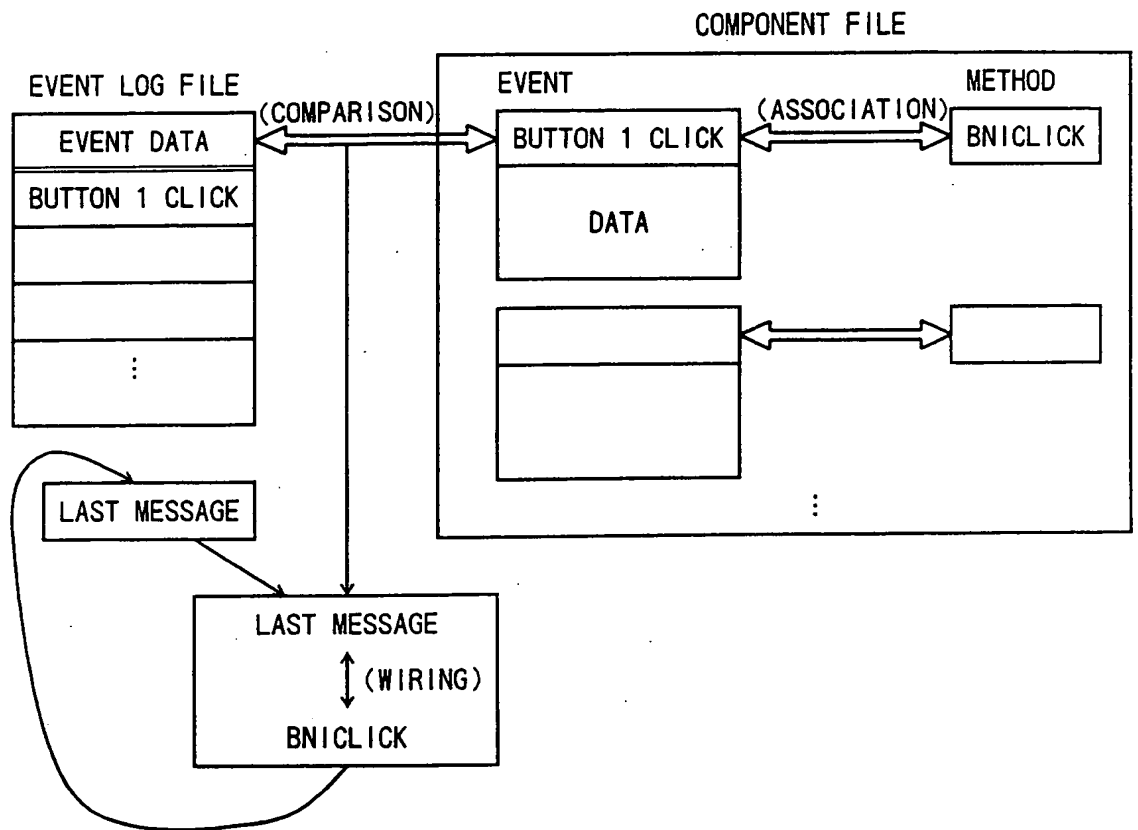


Fig. 110



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Fig.111



09765430-050701

Fig. 112

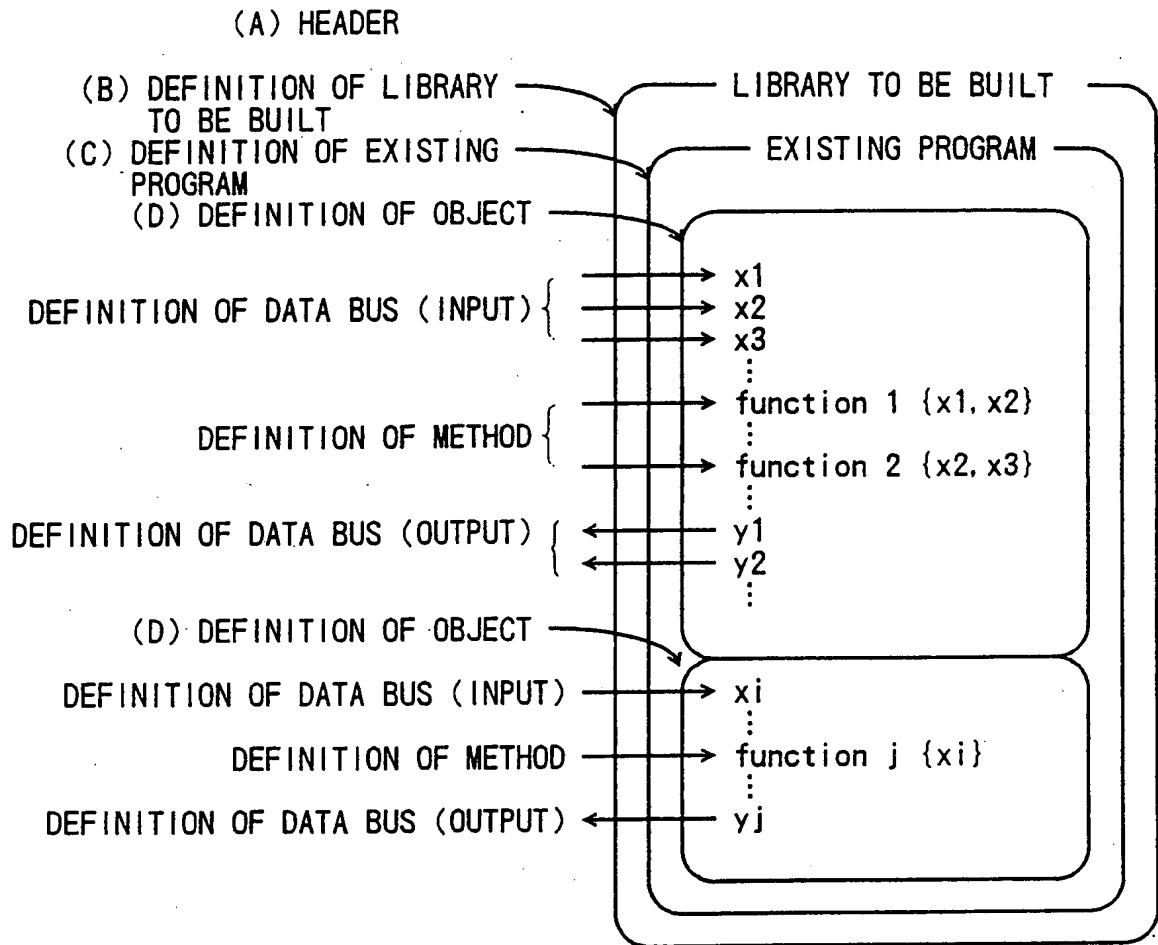


Fig.113

	ITEMS	KEYWORDS	REMARKS
(A)	PROJECT PROJECT NAME PATH OF COMPILER SYSTEM PATH OF FIRSTSIGHT SYSTEM PATH OF USER AREA	LSIBuilderProject LSIBuilderProjectName MSVCRoot CoreRoot UserRoot	
(B)	DEFINITION OF ARCHIVES NAME OF ARCHIVES PATH OF LIB PATH OF DLL	Archives ArchivesName LibPath DllPath	
(C)	NAME OF LIBRARY TO BE BUILT COMPILE MODE DEFINITION OF #define AND typedef	LibName Debug Header	
(D)	DEFINITION OF LSI NAME OF LSI COLOR OF LSI DATA BUS NAME OF DATA CORRECTION PROCESS NAME OF DATA BUS TYPE OF VARIABLES DATA CORRECTION PROCESS DIRECT DEVELOPMENT INTO DefineConnector DISTINCTION BETWEEN INPUT AND OUTPUT COLOR OF BUS INSTRUCTION NAME OF INSTRUCTION BUS FUNCTION NAME OF ENTRY POINT MEANING OF RETURN VALUE INSTRUCTION PROCESS Cmd ? DIRECT DEVELOPMENT INTO Cmd OR Command COLOR OF BUS GLOBAL VARIABLES (GLOBAL VARIABLES INSIDE LSI) DEFINITION #define AND typedef INITIALIZATION PROCESS CONSTRUCTOR DESTRUCTOR	LSI LSIName Color DataBus ProcessName Name VariableType Process Inline IO Color InstBus Name ProcessName ReturnValue Process Cmd Inline Colo Variables Header Initialize Constructor Destructor	TREE COLORS OF RGB (0-255) CODE OF FUNCTION input OR output zero OR nonzero OR NUMERAL CODE OF FUNCTION yes/no CODE OF FUNCTION CODE OF FUNCTION CODE OF FUNCTION

00755430-050704

Fig.114

<div style="display: flex; justify-content: space-between; font-weight: normal; font-size: 0.9em;"> FILE EDITING DISPLAY MAKE HELP </div>																																									
<div style="margin-bottom: 10px;"> <input type="radio"/> project NAME <div style="margin-left: 10px;"> <input type="radio"/> information <div style="margin-left: 10px;"> <input type="radio"/> archives NAME <div style="margin-left: 10px;"> <input type="radio"/> information <div style="margin-left: 10px;"> <input type="radio"/> LSI NAME <div style="margin-left: 10px;"> <input type="radio"/> information <div style="margin-left: 10px;"> <input type="radio"/> instruction <div style="margin-left: 10px;"> <input type="radio"/> data <div style="margin-left: 10px;"> <input type="radio"/> LSI NAME <div style="margin-left: 10px;"> <input type="radio"/> information <div style="margin-left: 10px;"> <input type="radio"/> instruction <div style="margin-left: 10px;"> <input type="radio"/> data <div style="margin-left: 10px;"> <input type="radio"/> archives NAME <div style="margin-left: 10px;"> <input type="radio"/> information <div style="margin-left: 10px;"> <input type="radio"/> LSI NAME <div style="margin-left: 10px;"> <input type="radio"/> information <div style="margin-left: 10px;"> <input type="radio"/> instruction <div style="margin-left: 10px;"> <input type="radio"/> data </div> </div> </div> </div> </div> </div> </div> </div> </div> </div> </div> </div></div></div></div></div></div></div>	<div style="margin-bottom: 10px;"> <div style="text-align: center; font-weight: bold; font-size: 0.8em;">LSI INFORMATION</div> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <tr> <th style="width: 30%;">LSI NAME</th> <th style="width: 30%;">LSI COLOR</th> <th style="width: 30%;">define...</th> <th style="width: 10%;">...</th> </tr> <tr> <td> </td> <td> </td> <td> </td> <td> </td> </tr> </table> </div> <div style="margin-bottom: 10px;"> <div style="text-align: center; font-weight: bold; font-size: 0.8em;">INSTRUCTION BUS TABLE:</div> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <tr> <th style="width: 30%;">BUS NAME</th> <th style="width: 30%;">ENTRY ...</th> <th style="width: 30%;">RETURN VALUE...</th> <th style="width: 10%;">...</th> </tr> <tr> <td>...</td> <td> </td> <td> </td> <td> </td> </tr> <tr> <td>...</td> <td> </td> <td> </td> <td> </td> </tr> <tr> <td> </td> <td> </td> <td> </td> <td> </td> </tr> </table> </div> <div> <div style="text-align: center; font-weight: bold; font-size: 0.8em;">DATA BUS TABLE:</div> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <tr> <th style="width: 30%;">BUS NAME</th> <th style="width: 30%;">TYPE OF VARIABLE</th> <th style="width: 30%;">INPUT & OUTPUT...</th> <th style="width: 10%;">...</th> </tr> <tr> <td>...</td> <td> </td> <td> </td> <td> </td> </tr> <tr> <td>...</td> <td> </td> <td> </td> <td> </td> </tr> <tr> <td> </td> <td> </td> <td> </td> <td> </td> </tr> </table> </div>	LSI NAME	LSI COLOR	define...	...					BUS NAME	ENTRY ...	RETURN VALUE...								BUS NAME	TYPE OF VARIABLE	INPUT & OUTPUT...							
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